

General Description

The MYH4040M is the high cell density trenched N-CH MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

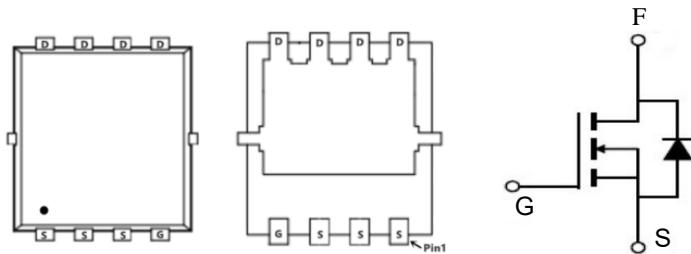


Features

X _{FUU}	42	X
K _F	62	C
T _{FUQP+CVI U? 10X+}	5.9	o á
T _{FUQP+CVI U? 4.7X+}	7.9	o á

Application

- Battery protection
- Load switch
- Uninterruptible power supply



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
MYH4040M	PDFN3333-8L	MYH4040M	5000

Absolute Maximum Ratings ($T_c=25^\circ\text{C}$)

Param	Value	Unit	Notes
V _G	±40V	V	Gate-Source Voltage
X _D	±20V	V	Continuous Drain Current, V _{GS} @ 10V ¹
I _D @T _A =70°C	35A	A	Continuous Drain Current, V _{GS} @ 10V ¹
I _{DM}	130A	A	Pulsed Drain Current ²
EAS	48mJ	mJ	Single Pulse Avalanche Energy ³
I _{AS}	35A	A	Avalanche Current
P _D @T _A =25°C	39W	W	Total Power Dissipation ⁴
T _{STG}	-55 to 150 °C	°C	Storage Temperature Range
T _J	-55 to 150 °C	°C	Operating Junction Temperature Range
R _{θJA}	60 °C/W	°C/W	Thermal Resistance Junction-ambient ¹
R _{θJC}	3.2 °C/W	°C/W	Thermal Resistance Junction-Case ¹

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	40	---	---	V
R _{DSON}	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =12A	---	5.9	7.0	mΩ
		V _{GS} =4.5V , I _D =10A	---	7.9	9.5	
V _{Gsth}	Gate Threshold Voltage		1.35	--	3	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =32V , V _{GS} =0V , T _J =25 °C	---	---	1	uA
		V _{DS} =32V , V _{GS} =0V , T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V	---	---	±100	nA
R _g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz	---	1.7	---	Ω
Q _g	Total Gate Charge (4.5V)	V _{DS} =20V , V _{GS} =4.5V , I _D =12A	---	5.8	---	nC
Q _{gs}	Gate-Source Charge		---	3	---	
Q _{gd}	Gate-Drain Charge		---	1.2	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =15V , V _{GS} =10V , R _G =3.3 I _D =10A	---	14.3	---	ns
T _r	Rise Time		---	5.6	---	
T _{d(off)}	Turn-Off Delay Time		---	20	---	
T _f	Fall Time		---	11	---	
C _{iss}	Input Capacitance	V _{DS} =15V , V _{GS} =0V , f=1MHz	---	690	---	pF
C _{oss}	Output Capacitance		---	193	---	
C _{rss}	Reverse Transfer Capacitance		---	38	---	
I _S	Continuous Source Current ^{1,5}	V _G =V _D =0V , Force Current	---	---	60	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =1A , T _J =25°C	---	---	1	V

Note :

- 1 .The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3 .The EAS data shows Max. rating . The test condition is V_{DD}=25V,V_{GS}=10V,L=0.1mH,I_{AS}=35A
- 4.The power dissipation is limited by 150°C junction temperature 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

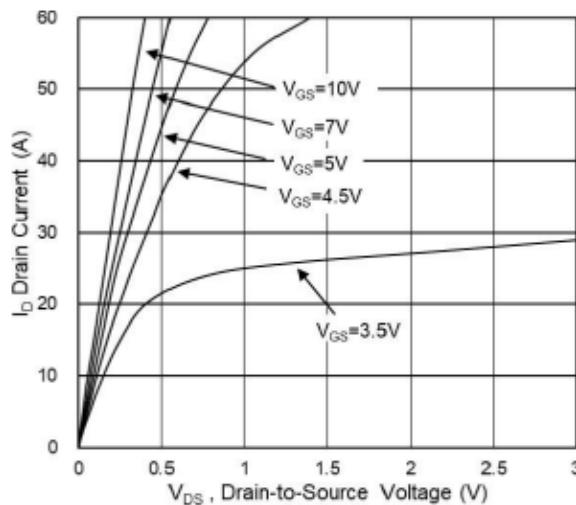


Fig.1 Typical Output Characteristics

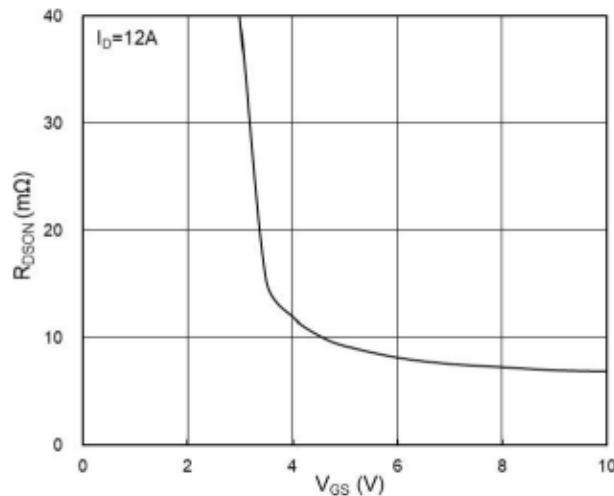


Fig.2 On-Resistance vs G-S Voltage

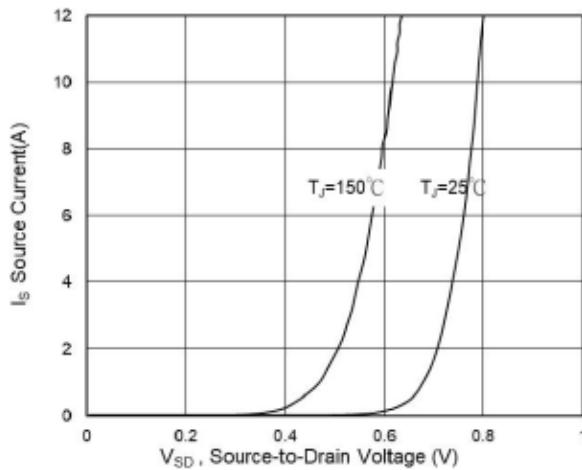


Fig.3 Source Drain Forward Characteristics

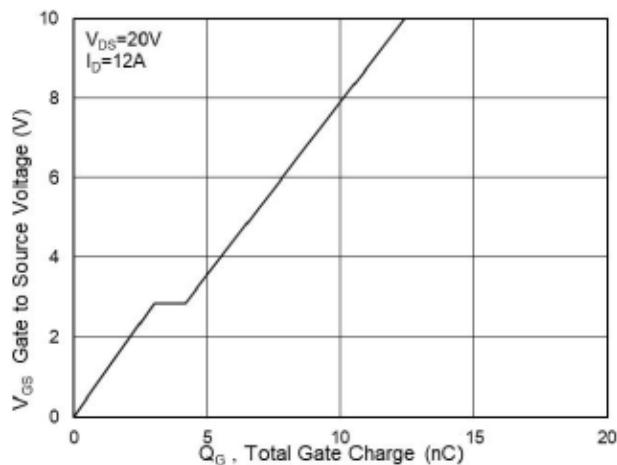


Fig.4 Gate-Charge Characteristics

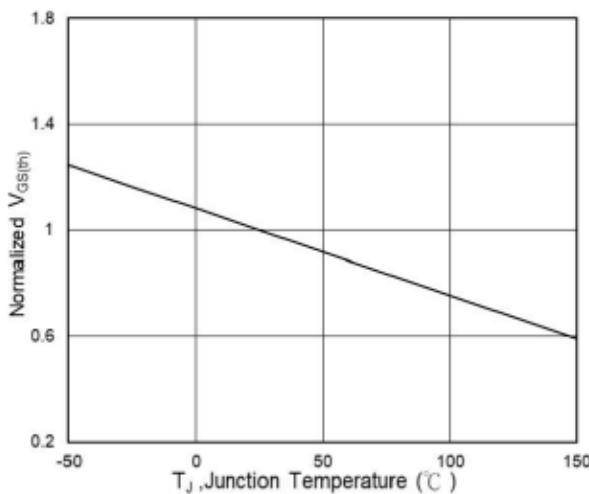


Fig.5 Normalized $V_{GS(th)}$ vs T_J

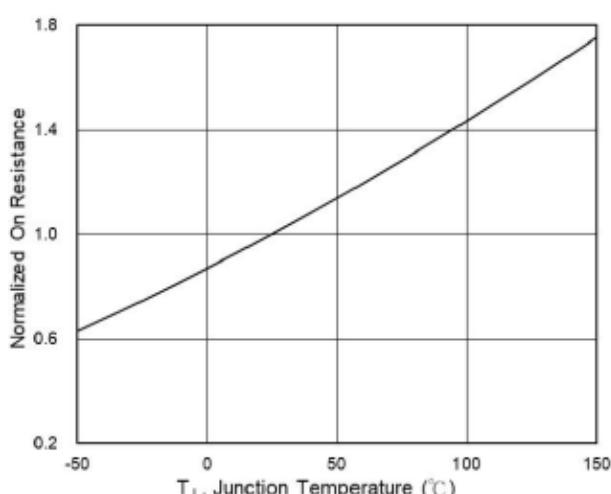


Fig.6 Normalized $R_{DS(on)}$ vs T_J

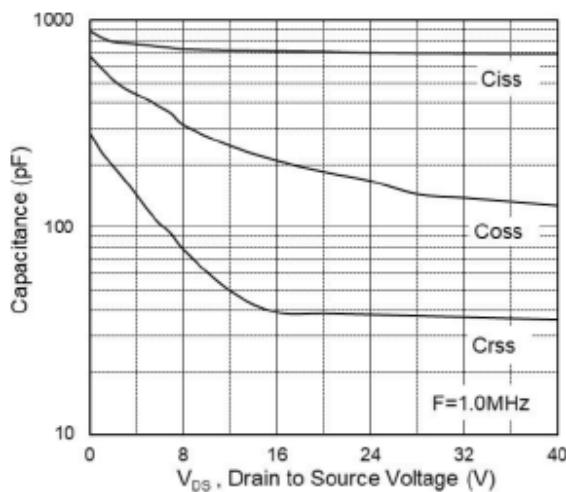


Fig.7 Capacitance

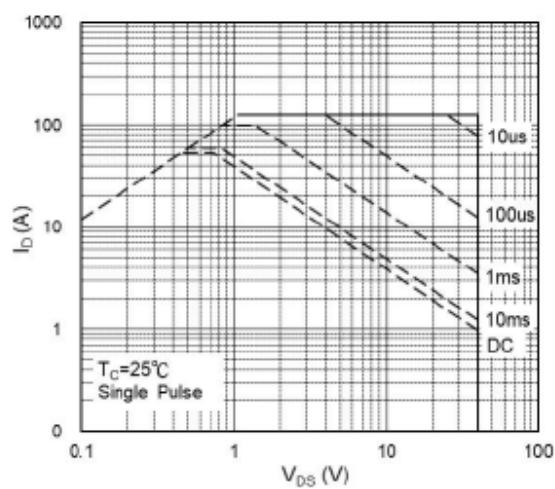


Fig.8 Safe Operating Area

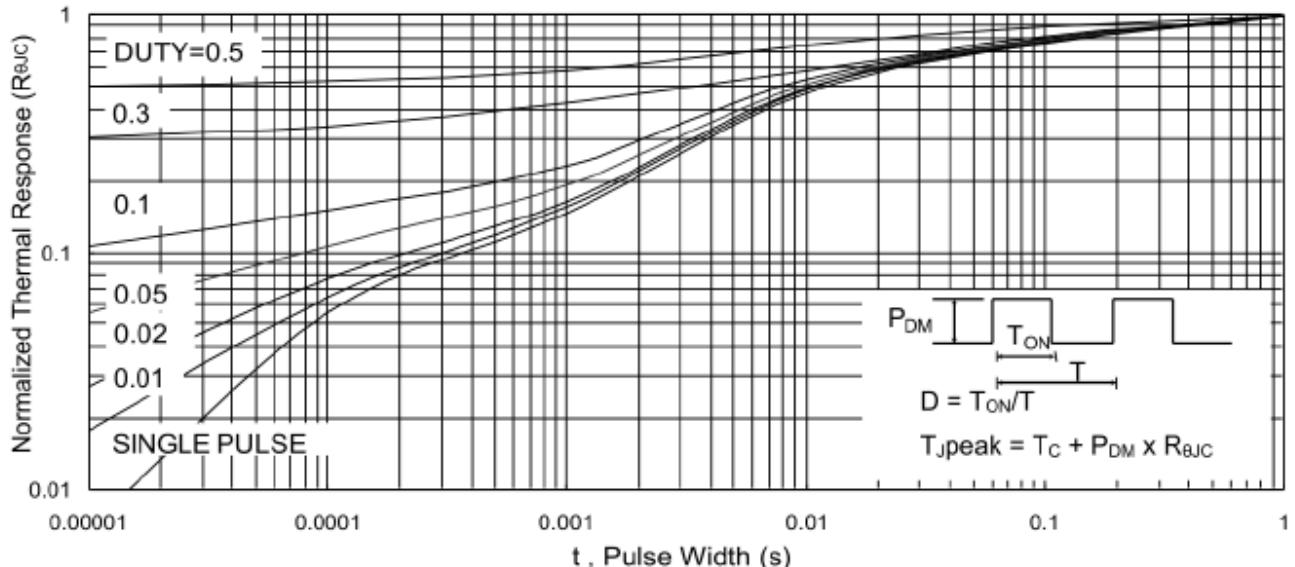


Fig.9 Normalized Maximum Transient Thermal Impedance

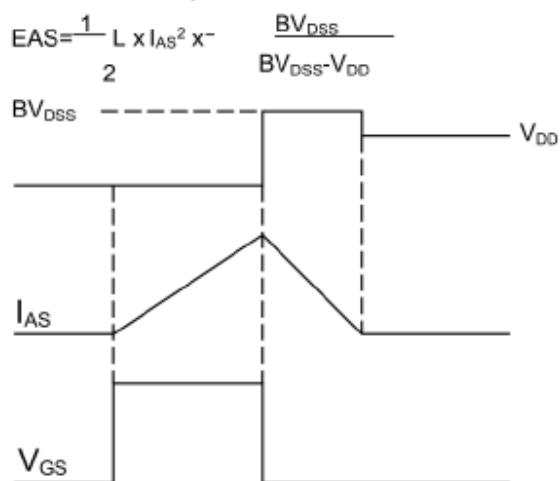
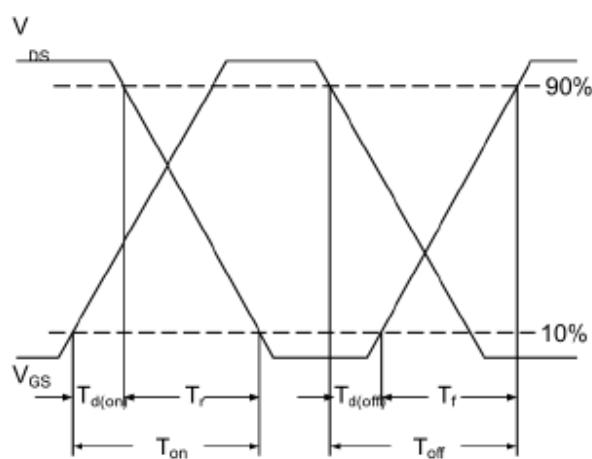
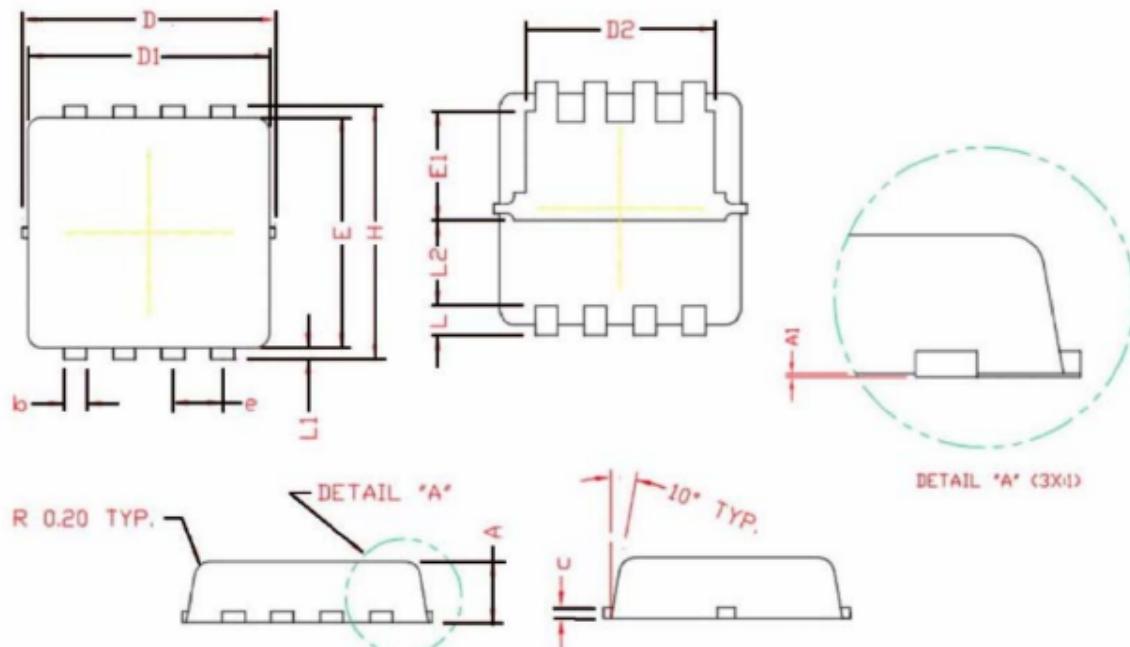


Fig 11 Unclamped Inductive Waveform

Package Mechanical Data-PDFN3333-8L Single



COMMON DIMENSIONS

(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	0.70	0.80	0.90
A1	0.00	0.03	0.05
b	0.24	0.30	0.35
c	0.10	0.15	0.20
D	3.25	3.32	3.40
D1	3.05	3.15	3.25
D2	2.40	2.50	2.60
E	3.00	3.10	3.20
E1	1.35	1.45	1.55
e	0.65 BSC.		
H	3.20	3.30	3.40
L	0.30	0.40	0.50
L1	0.10	0.15	0.20
L2	1.13 REF.		