

## General Description

The MY8806BBNE3 is the highest performance trench N-CH MOSFETS with extreme high cell density, which provide excellent  $R_{DS(on)}$  and gate charge for most of the small power switching and load switch applications.

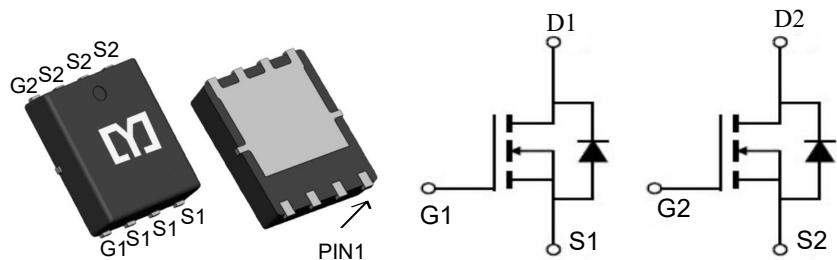


## Features

$V_{DSS}$	20	V
$I_D$	35	A
$R_{DS(ON)}(\text{at } V_{GS}=4.5V)$	<8.8	$m\Omega$
$R_{DS(ON)}(\text{at } V_{GS}=2.5V)$	<10.5	$m\Omega$

## Application

- Battery protection
- Load switch
- Uninterruptible power supply



## Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
MY8806BBNE3	PDFN3*3-8	NULL	5000

## Absolute Maximum Ratings ( $T_A=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	20	V
$V_{GS}$	Gate-Source Voltage	$\pm 8$	V
$I_D @ T_c = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V^1$	35	A
$I_D @ T_c = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V^1$	32.6	A
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V^1$	19	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V^1$	15	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	100	A
$P_D @ T_c = 25^\circ C$	Total Power Dissipation <sup>1</sup>	31	W
$P_D @ T_A = 25^\circ C$	Total Power Dissipation <sup>1</sup>	3.6	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	35	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	4	$^\circ C/W$

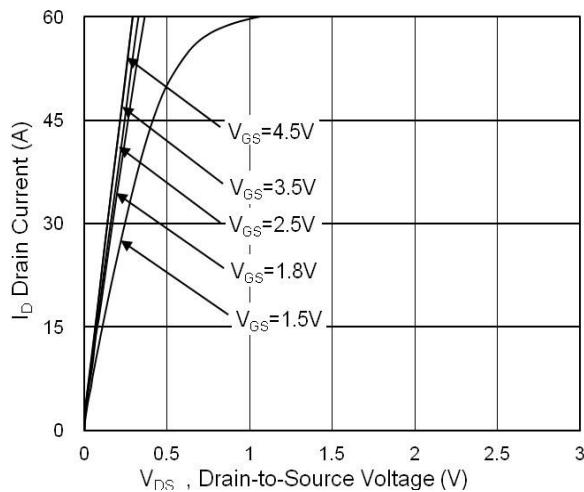
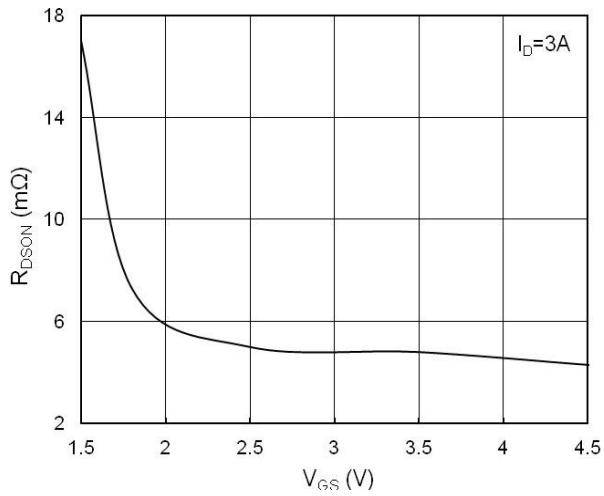
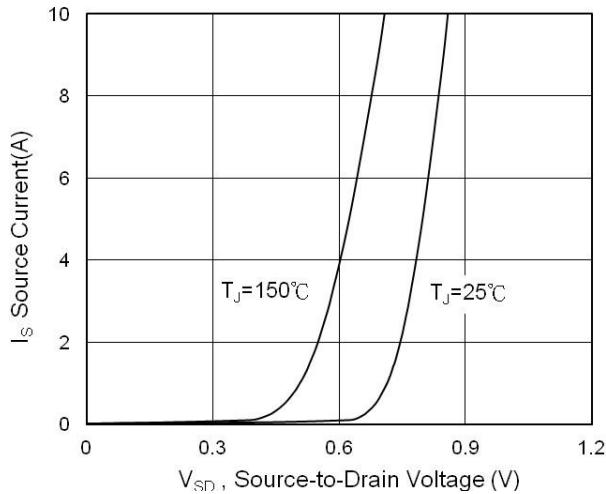
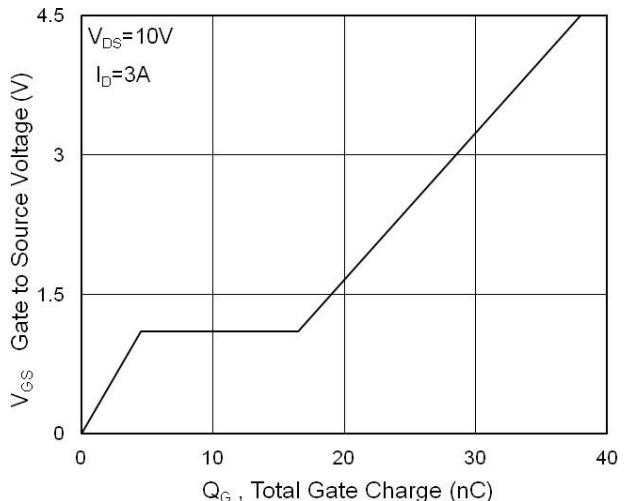
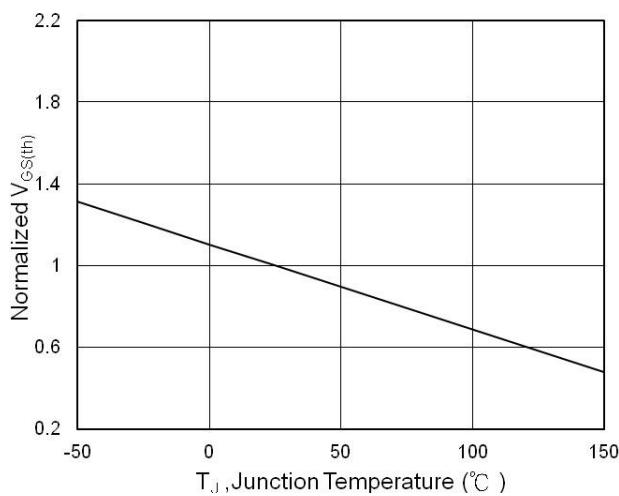
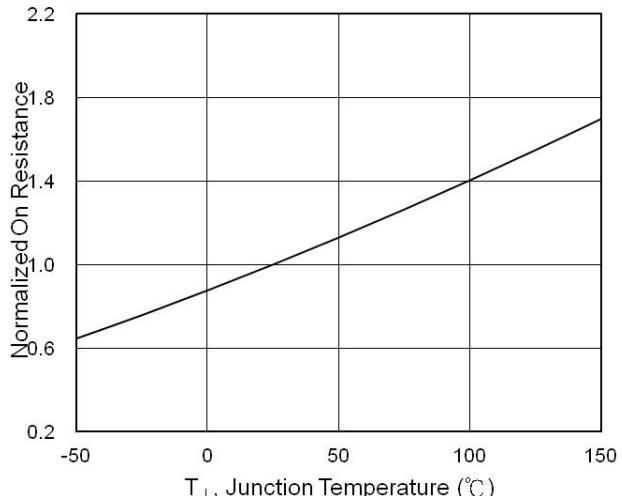
Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	20	---	---	V
$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{\text{GS}}=4.5\text{V}$ , $I_D=3\text{A}$	---	7.5	10.8	$\text{m}\Omega$
		$V_{\text{GS}}=3.9\text{V}$ , $I_D=3\text{A}$	---	10.53	16.5	
		$V_{\text{GS}}=2.5\text{V}$ , $I_D=3\text{A}$	---	18	20	
		$V_{\text{GS}}=1.8\text{V}$ , $I_D=3\text{A}$	---	22	24	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D=250\mu\text{A}$	0.4	---	1.0	V
$\text{I}_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=16\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{\text{DS}}=16\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=55^\circ\text{C}$	---	---	5	
$\text{I}_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 8\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 10$	$\mu\text{A}$
$\text{g}_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=5\text{V}$ , $I_D=3\text{A}$	---	42	---	S
$\text{Q}_{\text{g}}$	Total Gate Charge (4.5V)	$V_{\text{DS}}=10\text{V}$ , $I_D=3\text{A}$	---	38	---	$\text{nC}$
	Total Gate Charge (3.9V)		---	33	---	
$\text{Q}_{\text{gs}}$	Gate-Source Charge		---	4.5	---	
$\text{Q}_{\text{gd}}$	Gate-Drain Charge		---	12	---	
$\text{T}_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=16\text{V}$ , $V_{\text{GS}}=4.5\text{V}$ , $R_{\text{G}}=6\text{ }\Omega$ $I_D=3\text{A}$	---	22	---	$\text{ns}$
$\text{T}_r$	Rise Time		---	41	---	
$\text{T}_{\text{d(off)}}$	Turn-Off Delay Time		---	77	---	
$\text{T}_f$	Fall Time		---	21	---	
$\text{C}_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=10\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	3165	---	$\text{pF}$
$\text{C}_{\text{oss}}$	Output Capacitance		---	380	---	
$\text{C}_{\text{rss}}$	Reverse Transfer Capacitance		---	325	---	
$\text{I}_{\text{s}}$	Continuous Source Current <sup>1</sup>	$V_{\text{G}}=V_{\text{D}}=0\text{V}$ , Force Current	---	---	30	A
$\text{I}_{\text{SM}}$	Pulsed Source Current <sup>2</sup>		---	---	100	A
$\text{V}_{\text{SD}}$	Diode Forward Voltage <sup>2</sup>	$V_{\text{GS}}=0\text{V}$ , $I_{\text{s}}=3\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1.2	V

Note :

1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper,  $t \leq 10\text{s}$ .2.The data tested by pulsed , pulse width  $\leq 10\text{us}$  , duty cycle  $\leq 1\%$

### Typical Characteristics

**Fig.1 Typical Output Characteristics****Fig.2 On-Resistance vs. Gate-Source****Fig.3 Forward Characteristics Of Reverse****Fig.4 Gate-Charge Characteristics****Fig.5  $V_{GS(th)}$  vs.  $T_J$** **Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$**

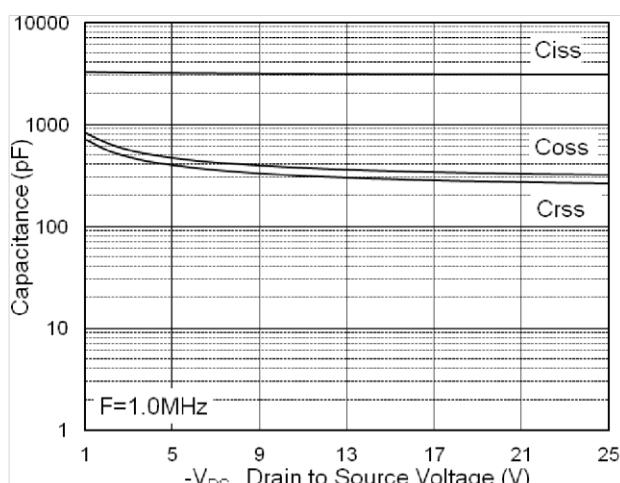
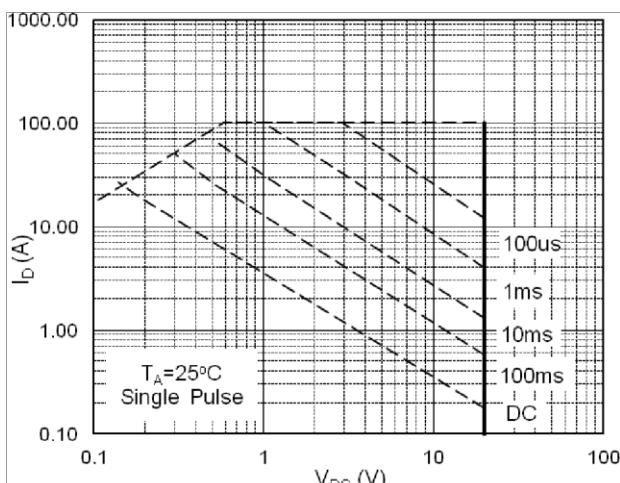


Fig.7 Capacitance

Fig.8 Safe Operating Area

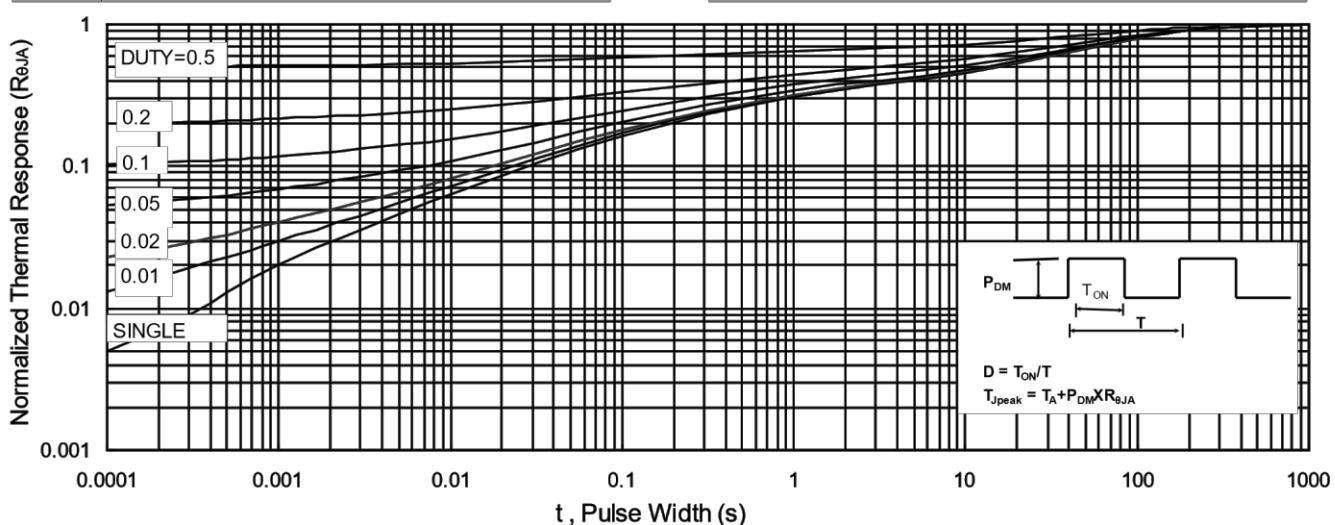


Fig.9 Normalized Maximum Transient Thermal Impedance

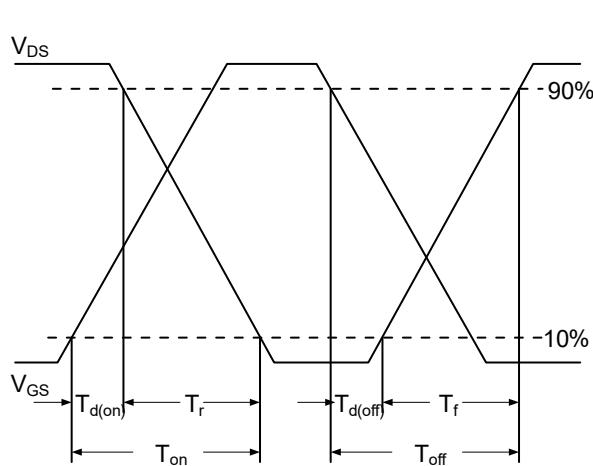


Fig.10 Switching Time Waveform

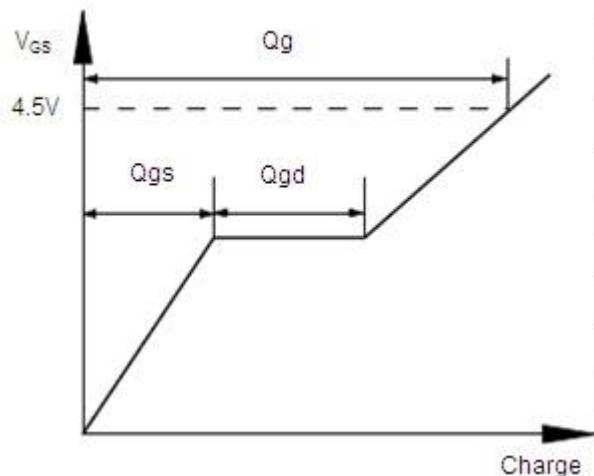
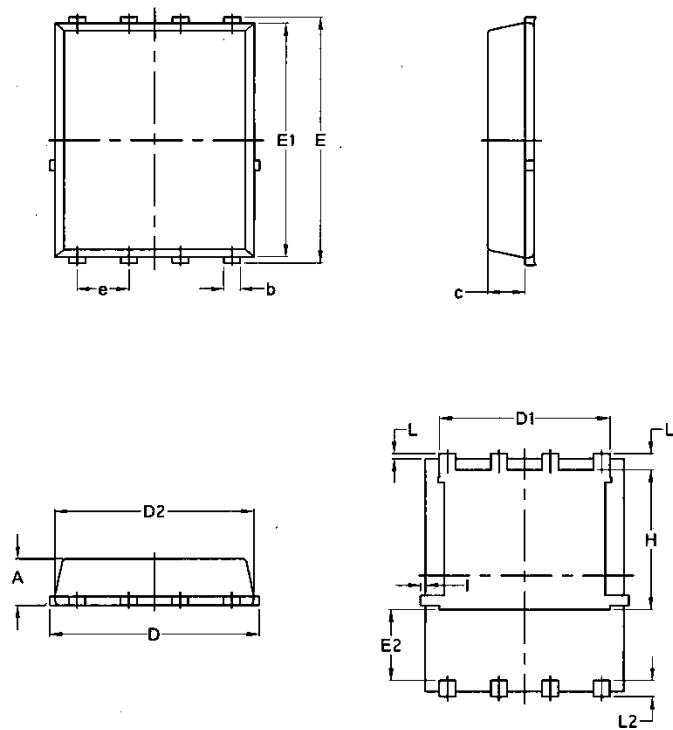


Fig.11 Gate Charge Waveform

## Package Mechanical Data-DFN5\*6-8L-JQ Single



Symbol	Common			
	mm		Inch	
	Mim	Max	Min	Max
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.0970	0.0324	0.082
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	/	0.0630	/
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
I	/	0.18	/	0.0070