

## General Description

The MY80N03NE5 series are from Advanced Power innovated design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance.

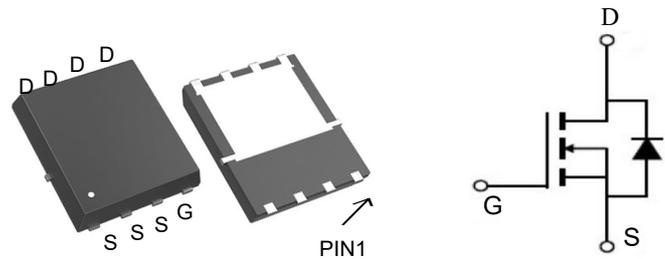


## Features

$V_{DSS}$	30	V
$I_D$	80	A
$R_{DS(ON)}(at V_{GS}=10V)$	3.2	$m\Omega$
$R_{DS(ON)}(at V_{GS}=4.5V)$	4.0	$m\Omega$

## Application

- Battery protection
- Load switch
- Uninterruptible power supply



## Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
MY80N03NE5	DFN5*6-8L	MY80N03NE5	5000

## Absolute Maximum Ratings ( $T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D@T_c=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	80	A
$I_D@T_c=100^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	50	A
$I_D@T_A=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	17	A
$I_D@T_A=70^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	14	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	160	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	144.7	mJ
$I_{AS}$	Avalanche Current	53.8	A
$P_D@T_c=25^\circ\text{C}$	Total Power Dissipation <sup>4</sup>	43.4	W
$P_D@T_A=25^\circ\text{C}$	Total Power Dissipation <sup>4</sup>	1.67	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	75	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	2.88	$^\circ\text{C/W}$

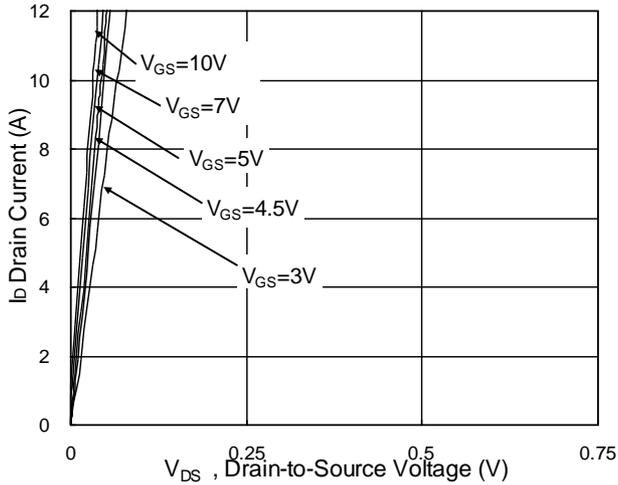
**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	30	---	---	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	BVDSS Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =1mA	---	0.0213	---	V/°C
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =30A	---	3.2	4.7	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =15A	---	4.0	6	
V <sub>GS(th)</sub>	Gate Threshold Voltage		1.0	---	2.5	V
ΔV <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	---	-5.73	---	mV/°C
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1	uA
		V <sub>DS</sub> =24V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C	---	---	5	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =30A	---	26.5	---	S
R <sub>g</sub>	Gate Resistance	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz	---	1.4	---	
Q <sub>g</sub>	Total Gate Charge (4.5V)		---	31.6	---	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =20V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =12A	---	6.07	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	13.8	---	
T <sub>d(on)</sub>	Turn-On Delay Time		---	11.2	---	ns
T <sub>r</sub>	Rise Time	V <sub>DD</sub> =15V, V <sub>GS</sub> =10V, R <sub>G</sub> =1.5	---	49	---	
T <sub>d(off)</sub>	Turn-Off Delay Time	I <sub>D</sub> =20A	---	35	---	
T <sub>f</sub>	Fall Time		---	7.8	---	
C <sub>iss</sub>	Input Capacitance		---	1260	---	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHz	---	139	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	98	---	
I <sub>S</sub>	Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	80	A
I <sub>SM</sub>	Pulsed Source Current <sup>2,5</sup>		---	---	160	A
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25°C	---	---	1	V

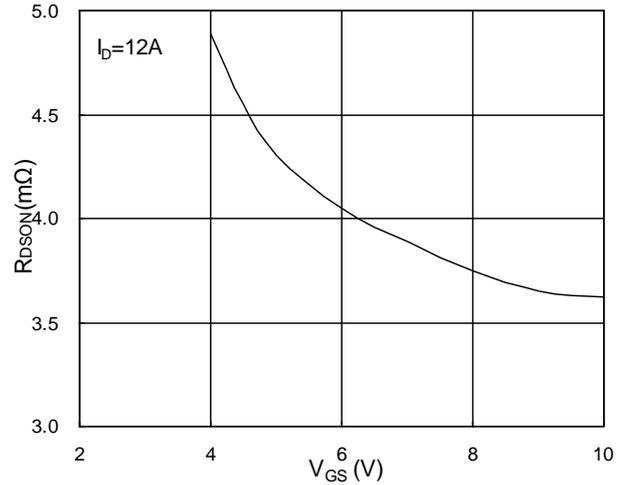
Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V<sub>DD</sub>=25V,V<sub>GS</sub>=10V,L=0.1mH,I<sub>AS</sub>=53.8A
- 4.The power dissipation is limited by 150°C junction temperature
- 5 .The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation.

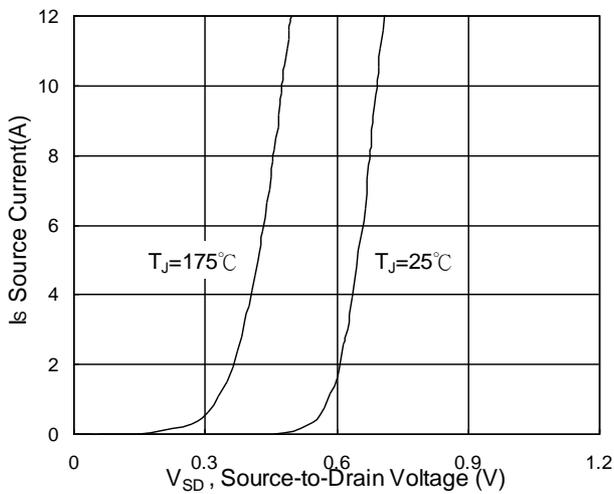
**Typical Characteristics**



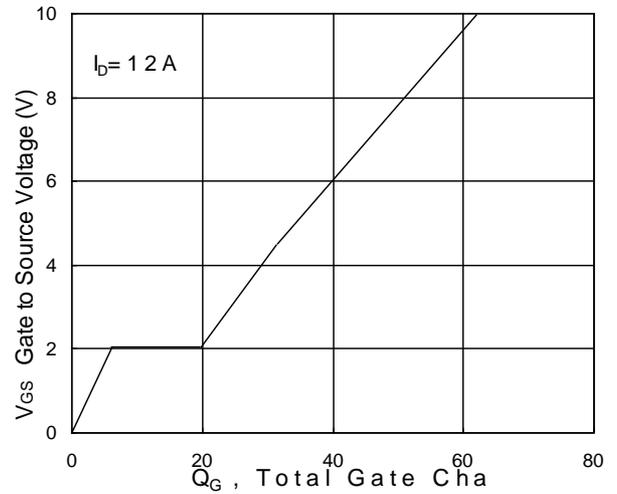
**Fig.1 Typical Output Characteristics**



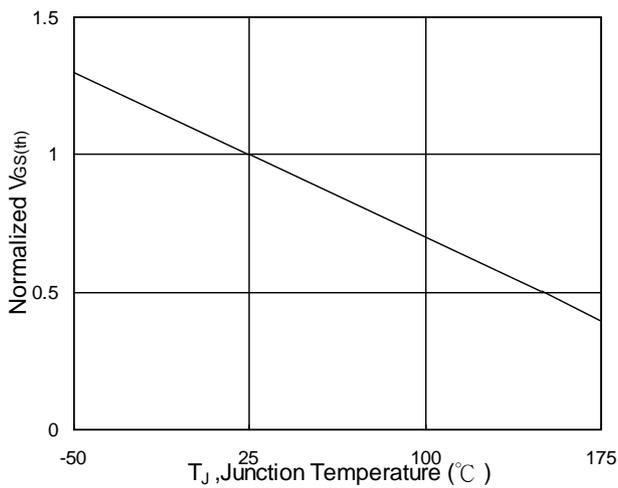
**Fig.2 On-Resistance vs. G-S Voltage**



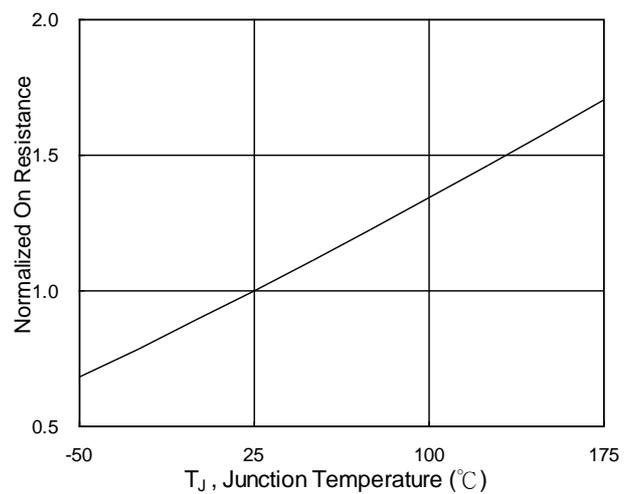
**Fig.3 Forward Characteristics of Reverse**



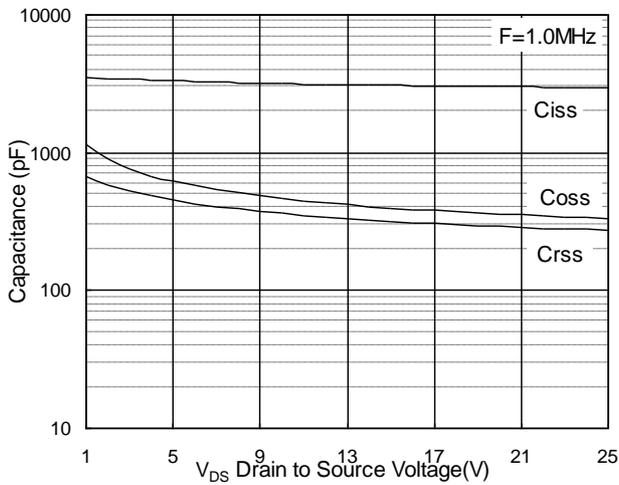
**Fig.4 Gate-Charge Characteristics**



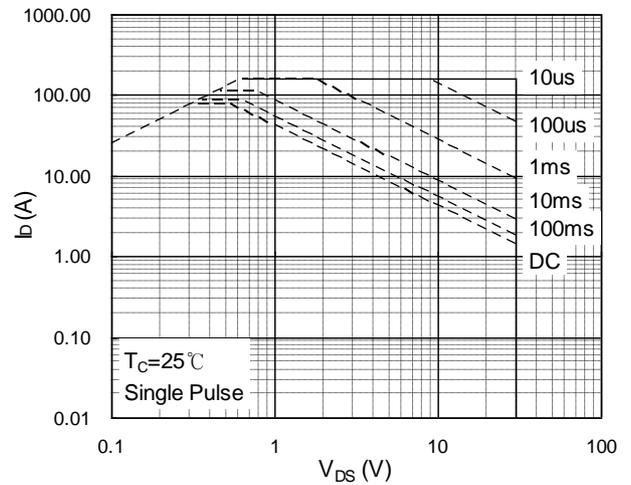
**Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$**



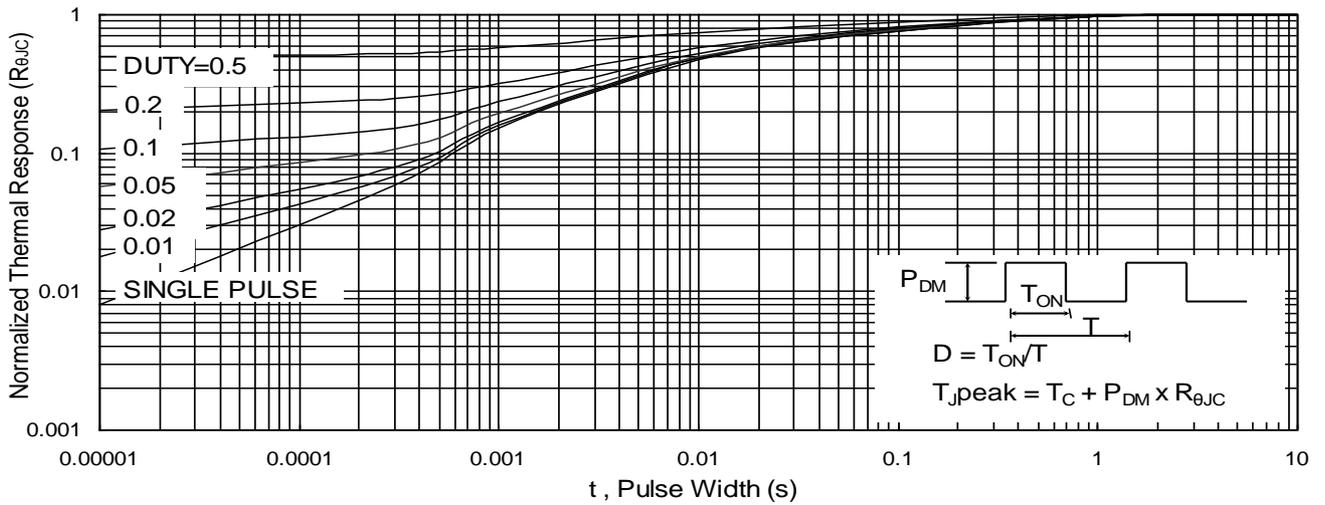
**Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$**



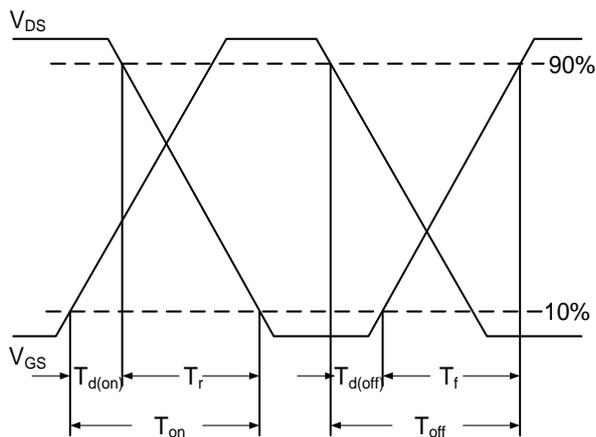
**Fig.7 Capacitance**



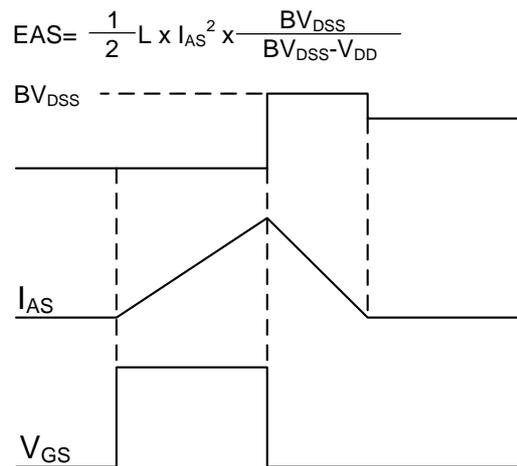
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**

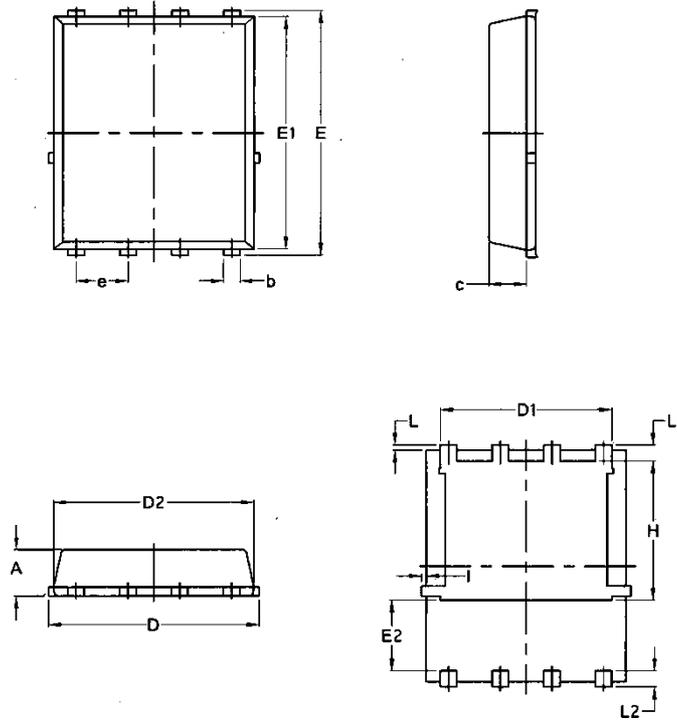


**Fig.10 Switching Time Waveform**



**Fig.11 Unclamped Inductive Switching Waveform**

**Package Mechanical Data-DFN5\*6-8L-JQ Single**



Symbol	Common			
	mm		Inch	
	Min	Max	Min	Max
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.0970	0.0324	0.082
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	/	0.0630	/
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
I	/	0.18	/	0.0070