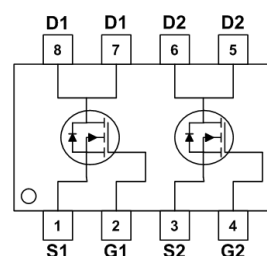
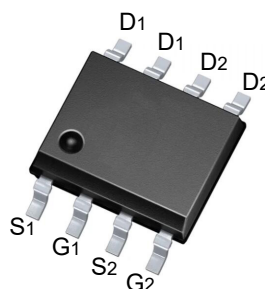


General Description

The MY4812 30V dual N-channel enhancement mode power field transistors in one package are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

Application

- Battery protection
- Load switch
- Uninterruptible power supply



Features

V_{DS}	30	V
I_D	8	A
$R_{DS(ON)}(at V_{GS}=10V)$	19	mΩ
$R_{DS(ON)}(at V_{GS}=4.5V)$	25	mΩ

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
MY4812	SOP-8	4812	3000

Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_A=25^{\circ}\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	9.5	A
$I_D@T_A=70^{\circ}\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	5.8	A
I_{DM}	Pulsed Drain Current ²	37	A
EAS	Single Pulse Avalanche Energy ³	12.1	mJ
I_{AS}	Avalanche Current	--	A
$P_D@T_A=25^{\circ}\text{C}$	Total Power Dissipation ⁴	2.8	W
T_{STG}	Storage Temperature Range	-55 to 150	$^{\circ}\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^{\circ}\text{C}$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹	---	--	$^{\circ}\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	46	$^{\circ}\text{C/W}$

Electrical Characteristics ($T_J=25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =30V, V _{GS} = 0V,	-	-	1.0	μA
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} = ±20V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.0	1.5	2.5	V
R _{DS(on)}	Static Drain-Source on-Resistance note3	V _{GS} =10V, I _D =5A	-	19	24	mΩ
		V _{GS} =4.5V, I _D =3A	-	25	29	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1.0MHz	-	490	-	pF
C _{oss}	Output Capacitance		-	79	-	pF
C _{rss}	Reverse Transfer Capacitance		-	61	-	pF
Q _g	Total Gate Charge	V _{DS} =15V, I _D =5.8A, V _{GS} =10V	-	10	-	nC
Q _{gs}	Gate-Source Charge		-	1.7	-	nC
Q _{gd}	Gate-Drain(“Miller”) Charge		-	2.5	-	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DS} =15V, I _D =3A, V _{GS} =10V, R _{REN} =3Ω	-	6	-	ns
t _r	Turn-on Rise Time		-	15	-	ns
t _{d(off)}	Turn-off Delay Time		-	17	-	ns
t _f	Turn-off Fall Time		-	17	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	9	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	36	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _S =9A	-	-	1.2	V
t _{rr}	Body Diode Reverse Recovery Time	I _F =5A, dI/dt=100A/μs	-	7	-	ns
Q _{rr}	Body Diode Reverse Recovery Charge		-	2	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : $T_J=25^{\circ}\text{C}, V_{DD}=15V, V_G=10V, L=0.5mH, R_g=25\Omega, I_{AS}=6A$

3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$

Typical Performance Characteristics

Figure 1: Output Characteristics

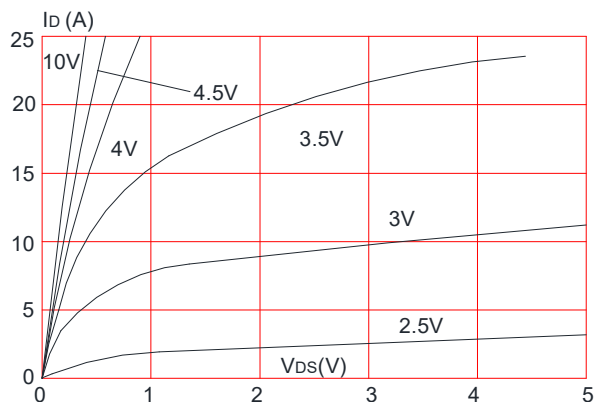


Figure 2: Typical Transfer Characteristics

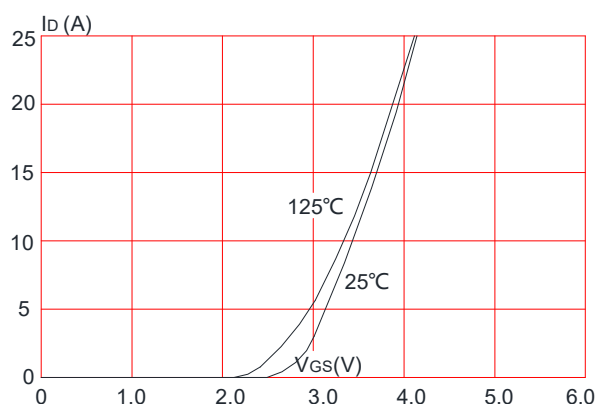


Figure 3: On-resistance vs. Drain Current

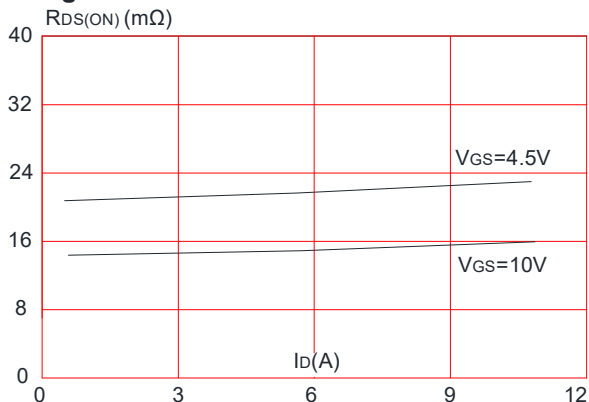


Figure 4: Body Diode Characteristics

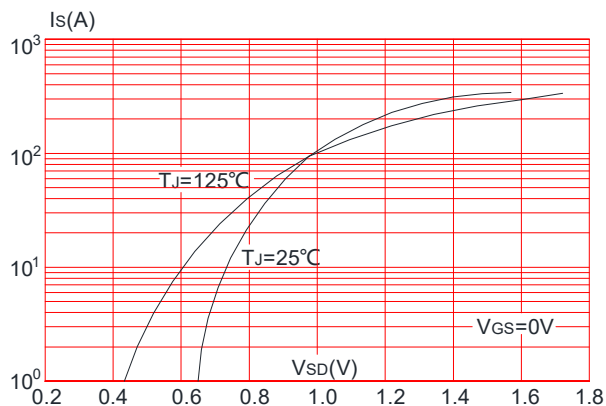


Figure 5: Gate Charge Characteristics

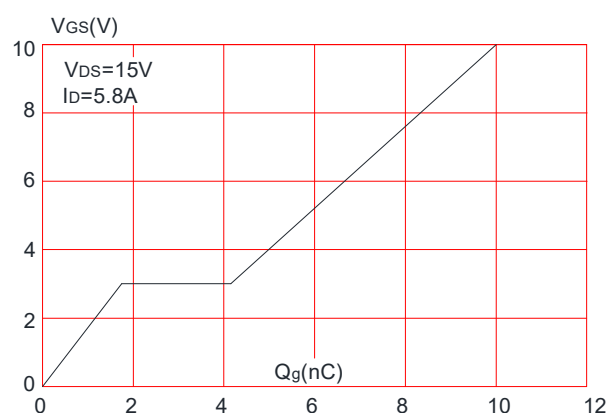


Figure 6: Capacitance Characteristics

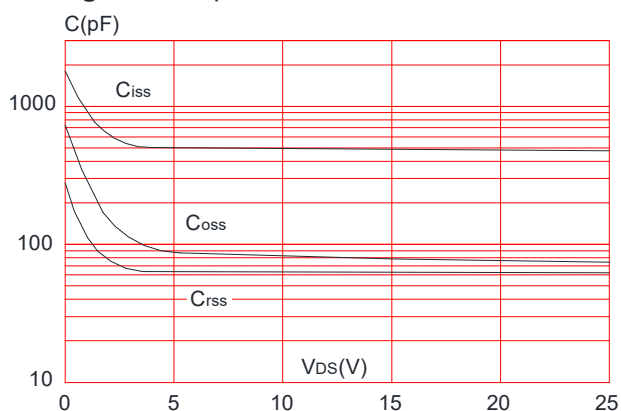


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

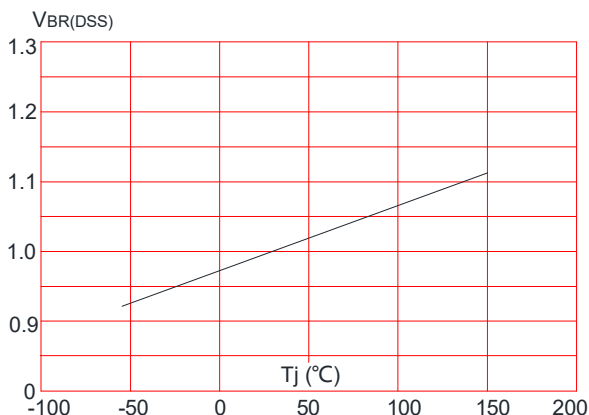


Figure 8: Normalized on Resistance vs. Junction Temperature

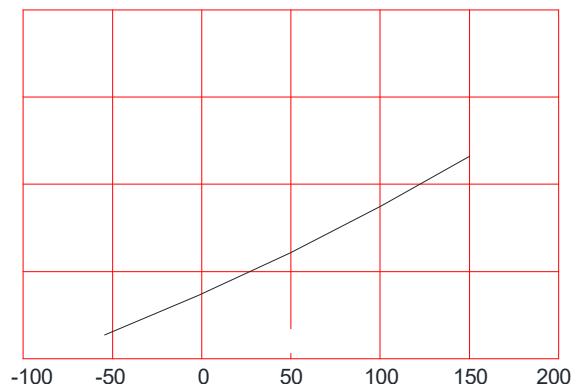


Figure 9: Maximum Safe Operating Area

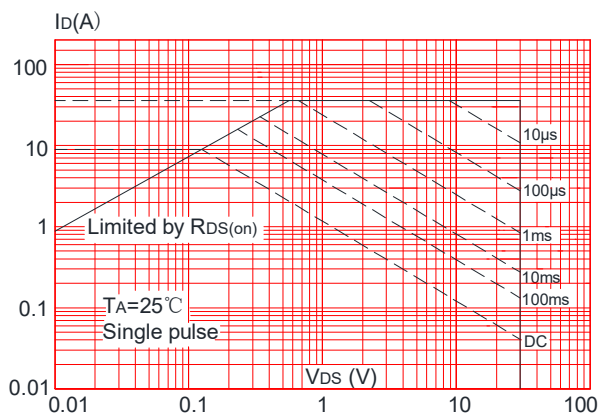


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

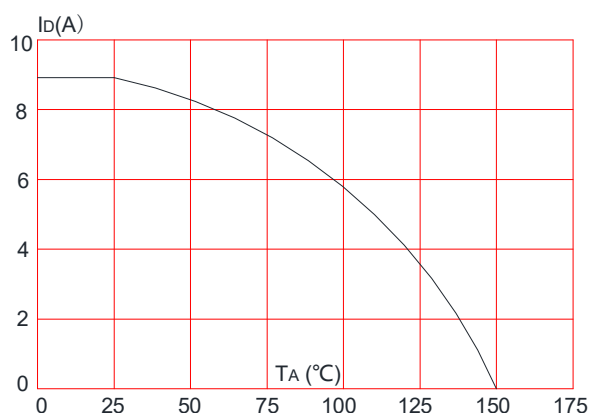
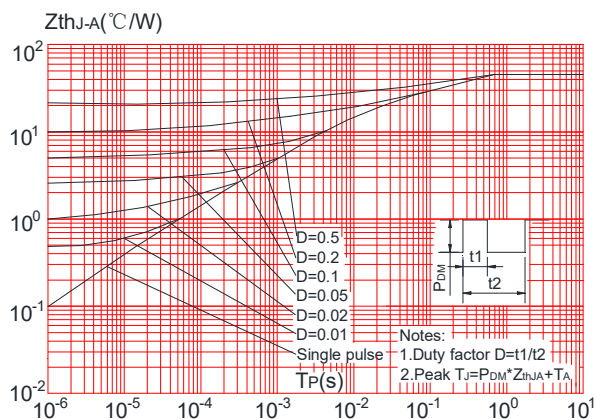
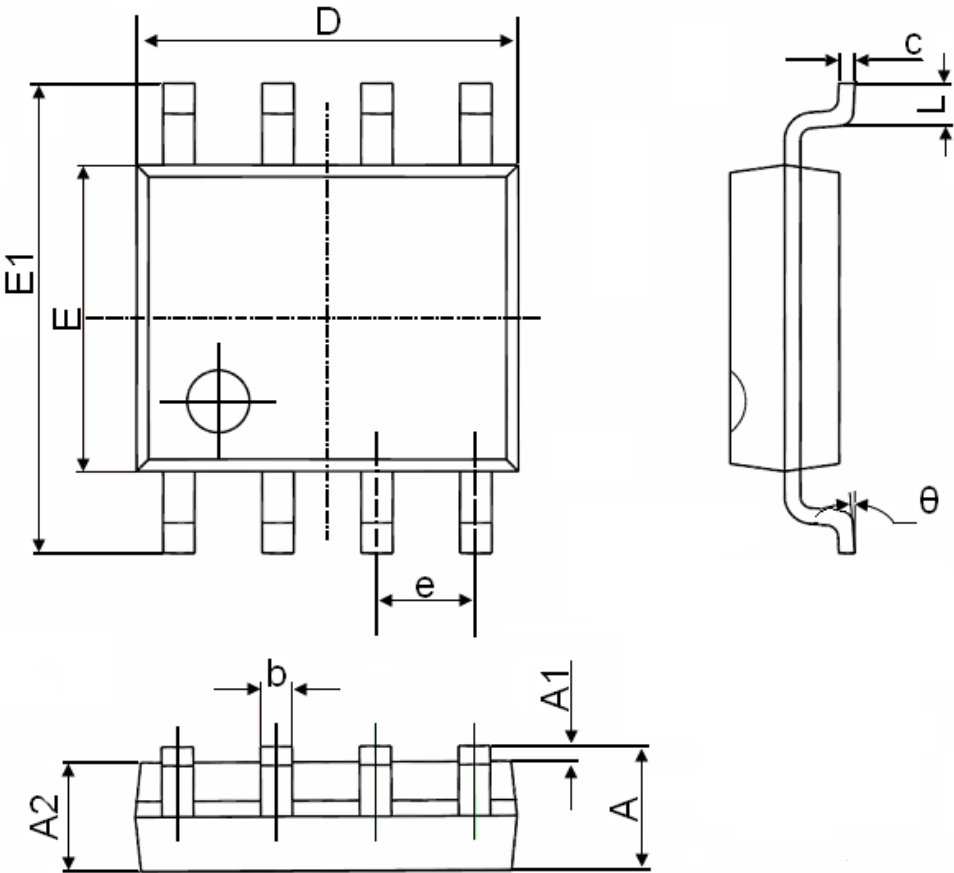


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



Package Mechanical Data-SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°