

General Description

The MY3N06A is the high cell density trenched N-CH MOSFET, which provides excellent $R_{DS(ON)}$ and efficiency for most of the small power switching and load switch applications.

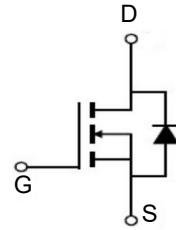
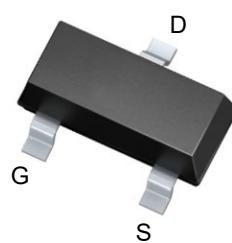


Features

V_{DSS}	60	V
I_D	3	A
$R_{DS(ON)}(\text{at } V_{GS}=10\text{V})$	<80	$\text{m}\Omega$
$R_{DS(ON)}(\text{at } V_{GS}=4.5\text{V})$	<100	$\text{m}\Omega$

Application

- DC-DC Converters
- Power management functions



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
MY3N06A	SOT-23-3L	080FN	3000

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

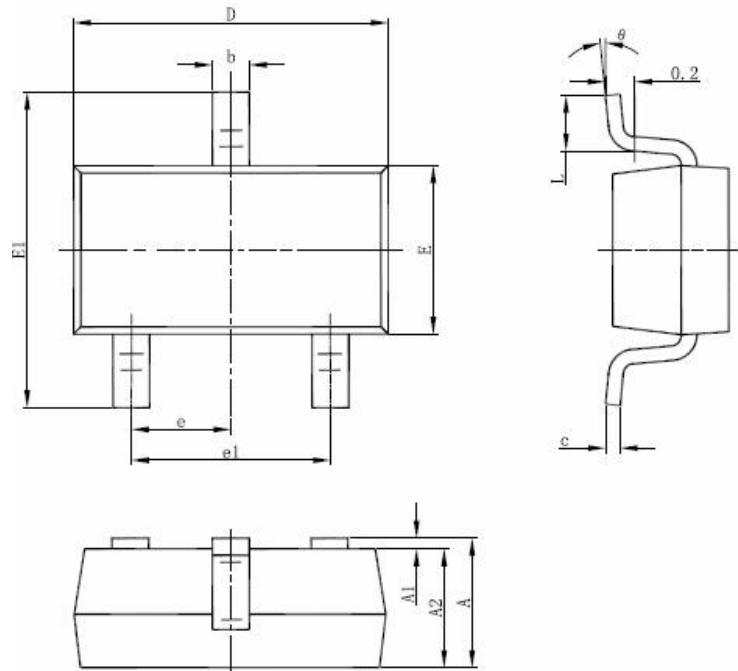
Parameter	Symbol	Limit	Unit
Drain-source Voltage	V_{DS}	60	V
Gate-source Voltage	V_{GS}	± 20	V
Drain Current	I_D	3.0	A
Pulsed Drain Current ^A	I_{DM}	12	A
Total Power Dissipation @ $T_c=25^\circ\text{C}$	P_D	1.2	W
Thermal Resistance Junction-to-Ambient ^B	$R_{\theta JA}$	105	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature Range	T_J, T_{STG}	-55~+150	$^\circ\text{C}$

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	60			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=60\text{V}, V_{\text{GS}}=0\text{V}$			1	μA
Gate-Body Leakage Current	I_{GSS1}	$V_{\text{GS}}= \pm 20\text{V}, V_{\text{DS}}=0\text{V}$			± 100	nA
	I_{GSS2}	$V_{\text{GS}}= \pm 12\text{V}, V_{\text{DS}}=0\text{V}$			± 50	nA
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}= V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.1	1.7	2.3	V
Static Drain-Source On-Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}= 10\text{V}, I_{\text{D}}=2\text{A}$			80	$\text{m}\Omega$
		$V_{\text{GS}}= 4.5\text{V}, I_{\text{D}}=1.5\text{A}$			100	
Diode Forward Voltage	V_{SD}	$I_{\text{S}}=2.0\text{A}, V_{\text{GS}}=0\text{V}$		0.8	1.2	V
Maximum Body-Diode Continuous Current	I_{S}				2.0	A
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$		330		pF
Output Capacitance	C_{oss}			90		
Reverse Transfer Capacitance	C_{rss}			17		
Switching Parameters						
Total Gate Charge	Q_{g}	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=30\text{V}, I_{\text{D}}=2.0\text{A}$		5.1		nC
Gate-Source Charge	Q_{gs}			1.3		
Gate-Drain Charge	Q_{gd}			1.7		
Turn-on Delay Time	$t_{\text{D(on)}}$	$V_{\text{GS}}=10\text{V}, V_{\text{DD}}=30\text{V}, I_{\text{D}}=1.5\text{A}, R_{\text{L}}=1\Omega, R_{\text{GEN}}=3\Omega$		13		ns
Turn-on Rise Time	t_{r}			51		
Turn-off Delay Time	$t_{\text{D(off)}}$			19		
Turn-off fall Time	t_{f}			12		

- A. Pulse Test: Pulse Width $\leq 300\text{us}$, Duty cycle $\leq 2\%$.
B. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.

Package Mechanical Data-SOT-23-3L



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°