MY20G04NE5

40V Dual N+P-Channel Enhancement Mode MOSFET

General Description

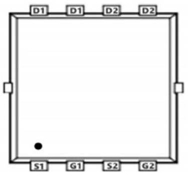
The MY20G04NE5 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

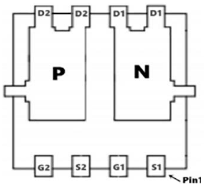
Features

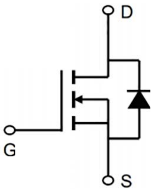
V_{DSS}	40	-40	V
I_D	11	-15	A
$R_{DS(ON)}$(atV_{GS}=10V)	12.5		mΩ
$R_{DS(ON)}$(atV_{GS}=-10V)	29.5		mΩ

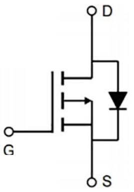
Application

- Battery protection
- Load switch
- Uninterruptible power supply









Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
MY20G04NE5	PDFN5*6-8L	20G04D	5000

Absolute Maximum Ratings (T_A=25℃unless otherwise noted)

Symbol	Parameter	Rating		Units
		N-Ch	P-Ch	
V_{DS}	Drain-Source Voltage	40	-40	V
V_{GS}	Gate-Source Voltage	±20	±20	V
I_D@T_C=25˚C	Continuous Drain Current, V_{GS} @ 10V[1]	11	-15	A
I_D@T_C=100˚C	Continuous Drain Current, V_{GS} @ 10V[1]	18	-16	A
I_{DM}	Pulsed Drain Current[2]	46	-40	A
EAS	Single Pulse Avalanche Energy[3]	28	66	mJ
I_{AS}	Avalanche Current	17.8	-27.2	A
P_D@T_C=25˚C	Total Power Dissipation[4]	25	31.3	W
T_{STG}	Storage Temperature Range	-55 to 150	-55 to 150	˚C
T_J	Operating Junction Temperature Range	-55 to 150	-55 to 150	˚C
$R_{\theta JA}$	Thermal Resistance Junction-Ambient [1]	---	62	˚C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case[1]	---	5	˚C/W

Electrical Characteristics (T_A=25 ℃, unless otherwise noted)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=250μA	40	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=40V, V_{GS}=0V	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	V_{DS}=0V, V_{GS}=±20V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=250μA	1.0	1.6	2.1	V
$R_{DS(on)}$	Static Drain-Source on-Resistance note3	V_{GS}=10V, I_D=7A	-	12.5	17	mΩ
		V_{GS}=4.5V, I_D=6A	-	17.5	24	mΩ
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS}=20V, V_{GS}=0V, f=1.0MHz	-	633	-	pF
C_{oss}	Output Capacitance		-	67	-	pF
C_{rss}	Reverse Transfer Capacitance		-	58	-	pF
Q_g	Total Gate Charge	V_{DS}=20V, I_D=8A, V_{GS}=10V	-	12	-	nC
Q_{gs}	Gate-Source Charge		-	3.2	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	3.1	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	V_{DD}= 20V, R_L =2.5Ω V_{GS}=10V,R_{REN} =3Ω	-	4	-	ns
t_r	Turn-on Rise Time		-	3	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	15	-	ns
t_f	Turn-off Fall Time		-	2	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	20	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	32	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS}=0V, I_S= 8A	-	-	1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : T_J=25℃,V_{DD}=20V,V_G=10V,L=0.5mH,Rg=25Ω,I_{AS}=7.2A
T_J=25℃,V_{DD}=-20V,V_G= -10V,L=0.5mH,Rg=25Ω,I_{AS}=-8.4A

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%

Electrical Characteristics (T_C=25 ˚C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=-250uA	-40	---	---	V
$\triangle BV_{DSS}/\triangle T_J$	BV_{DSS} Temperature Coefficient	Reference to 25˚C , I_D=-1mA	---	-0.012	---	V/˚C
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=-10V , I_D=-8A	---	29.5	39.5	mΩ
		V_{GS}=-4.5V , I_D=-4A	---	39.5	49.5	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =-250uA	-1.0	-1.8	-2.5	V
$\triangle V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	4.32	---	mV/˚C
I_{DSS}	Drain-Source Leakage Current	V_{DS}=-32V , V_{GS}=0V , T_J=25 ˚C	---	---	1	uA
		V_{DS}=-32V , V_{GS}=0V , T_J=55˚C	---	---	5	
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA
gfs	Forward Transconductance	V_{DS}=-5V , I_D=-8A	---	12.6	---	S
R_g	Gate Resistance	V_{DS}=0V , V_{GS}=0V , f=1MHz	---	13	16	
Q_g	Total Gate Charge (-4.5V)	V_{DS}=-20V , V_{GS}=-4.5V , I_D=-12A	---	9	---	nC
Q_{gs}	Gate-Source Charge		---	2.54	---	
Q_{gd}	Gate-Drain Charge		---	3.1	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DD}=-15V , V_{GS}=-10V , R_G=3.3 I_D=-1A	---	19.2	---	ns
T_r	Rise Time		---	12.8	---	
$T_{d(off)}$	Turn-Off Delay Time		---	48.6	---	
T_f	Fall Time		---	4.6	---	
C_{iss}	Input Capacitance	V_{DS}=-15V , V_{GS}=0V , f=1MHz	---	1004	---	pF
C_{oss}	Output Capacitance		---	108	---	
C_{rss}	Reverse Transfer Capacitance		---	80	---	
I_S	Continuous Source Current[1,5]	V_G=V_D=0V , Force Current	---	---	-20	A
I_{SM}	Pulsed Source Current[2,5]		---	---	-40	A
V_{SD}	Diode Forward Voltage[2]	V_{GS}=0V , I_S=-1A , T_J=25˚C	---	---	-1	V

Note :

1.The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.

2.The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%

3.The EAS data shows Max. rating . The test condition is VDD=-25V,VGS=-10V,L=0.1mH,IAS=-27.2A

4.The power dissipation is limited by 150℃ junction temperature

5 .The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

N-Channel Typical Characteristics

Figure1: Output Characteristics

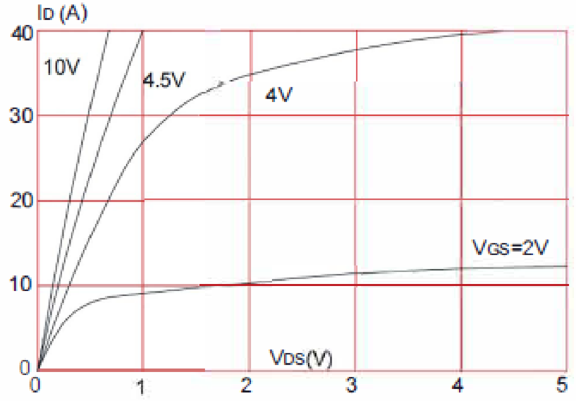


Figure 2: Typical Transfer Characteristics

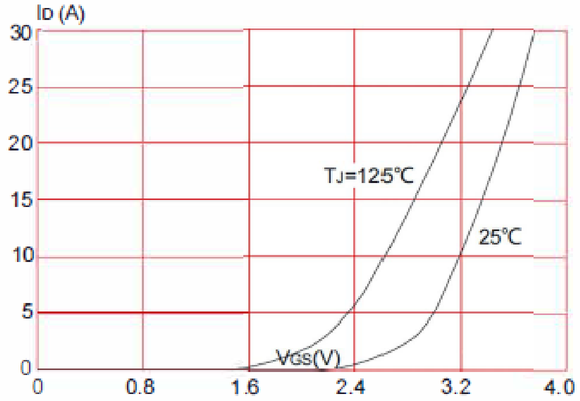


Figure 3: On-resistance vs. Drain Current

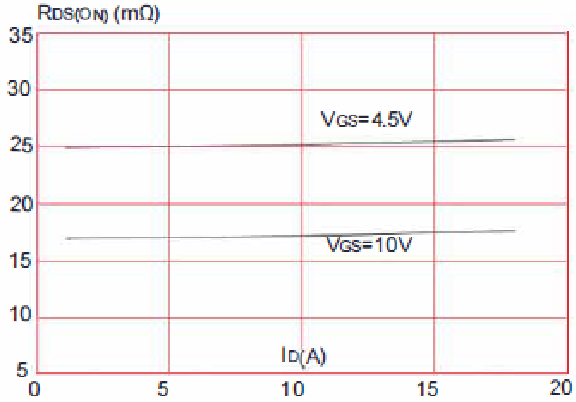


Figure 4: Body Diode Characteristics

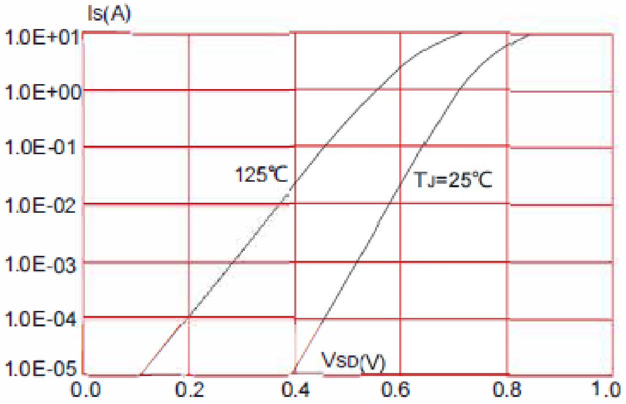


Figure 5: Gate Charge Characteristics

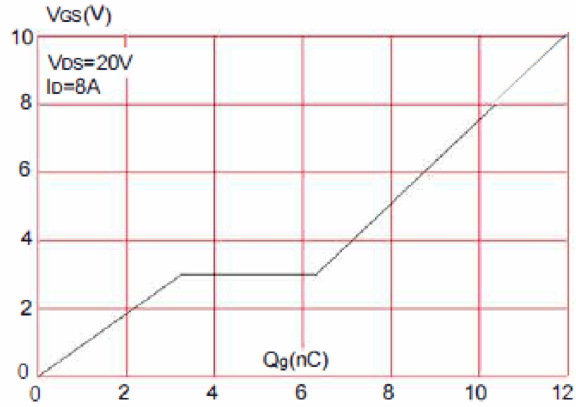


Figure 6: Capacitance Characteristics

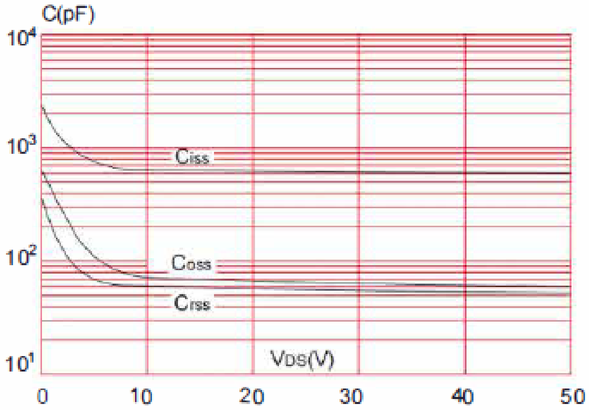


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

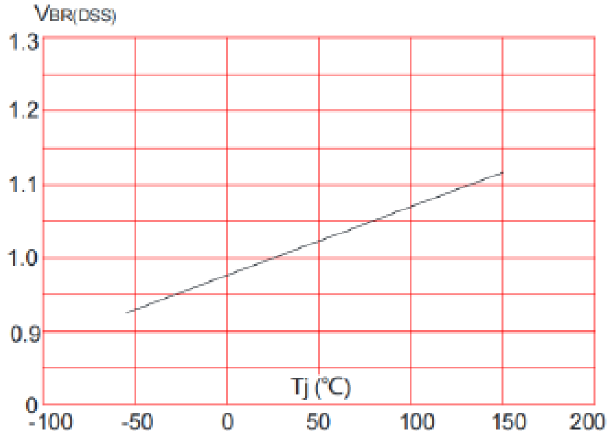


Figure 8: Normalized on Resistance vs. Junction Temperature

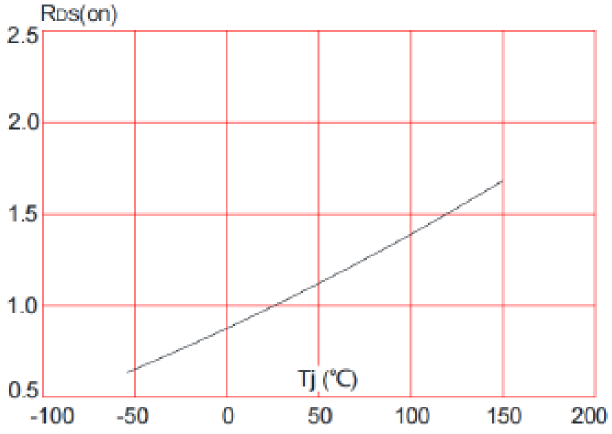


Figure 9: Maximum Safe Operating Area

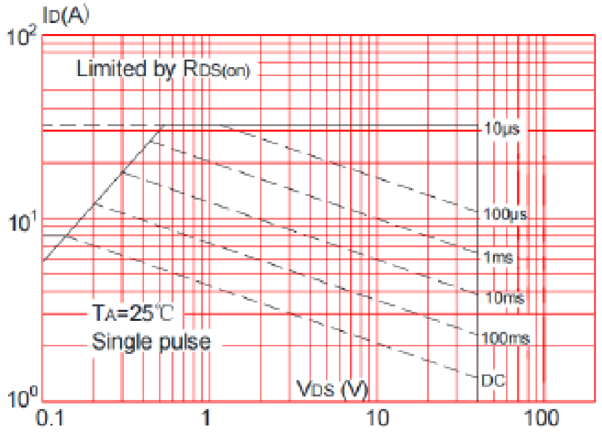


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

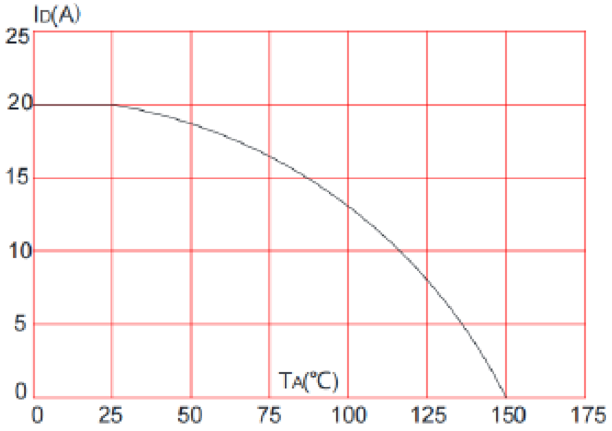
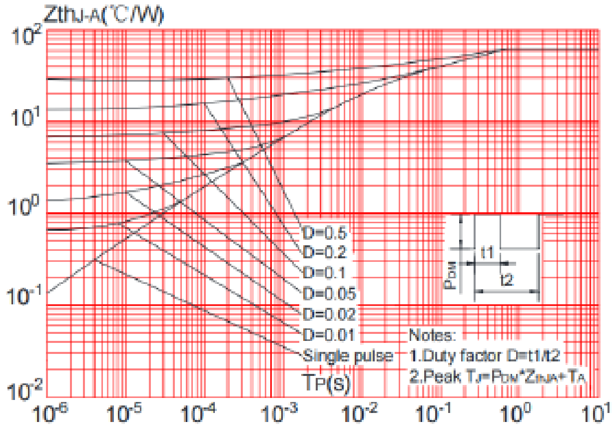


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



P-Channel Typical Characteristics

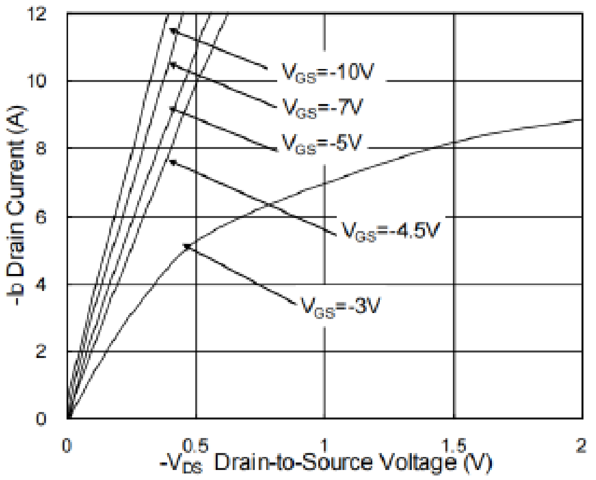


Fig.1 Typical Output Characteristics

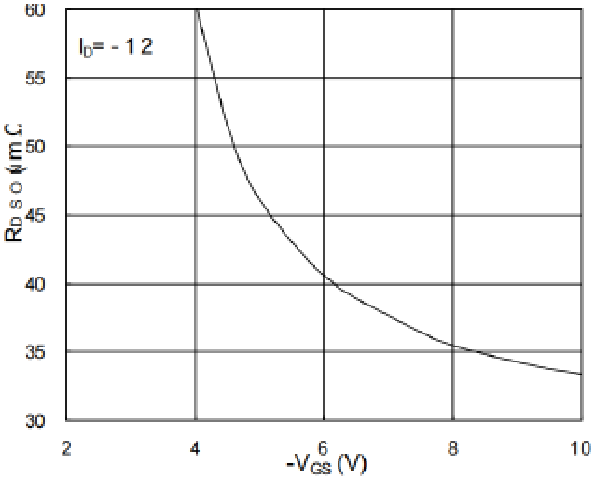


Fig.2 On-Resistance v.s Gate-Source

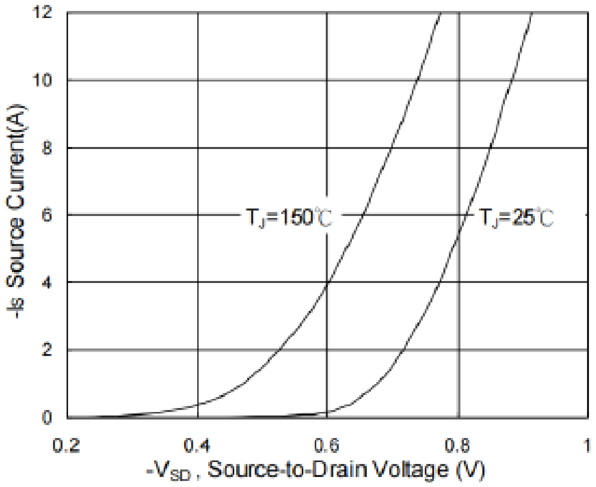


Fig.3 Forward Characteristics of Reverse

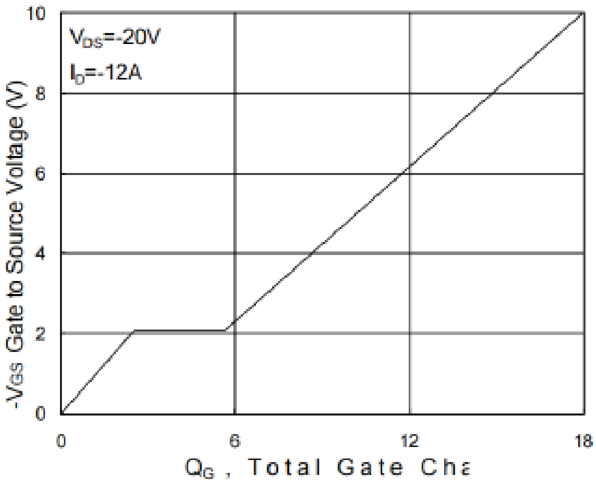


Fig.4 Gate-Charge Characteristics

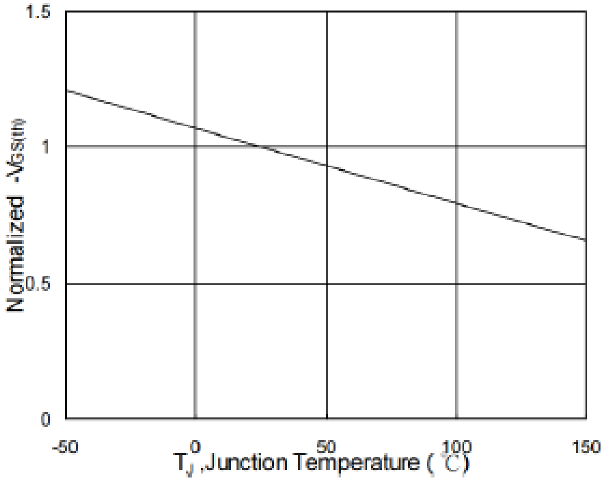


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

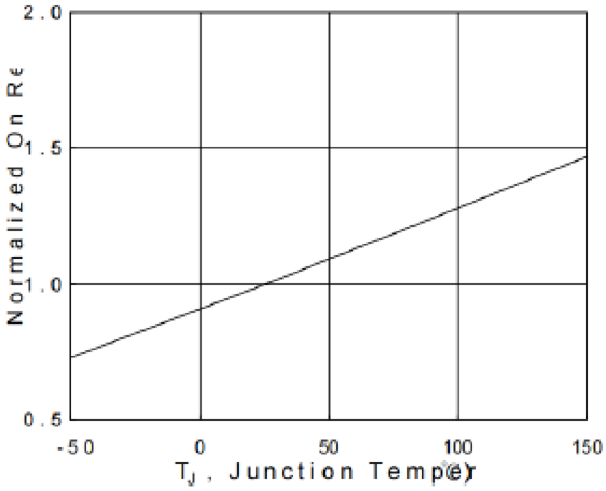


Fig.6 Normalized R_{DSON} v.s T_J

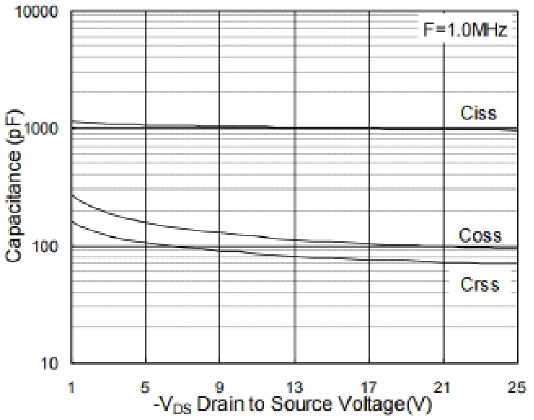


Fig.7 Capacitance

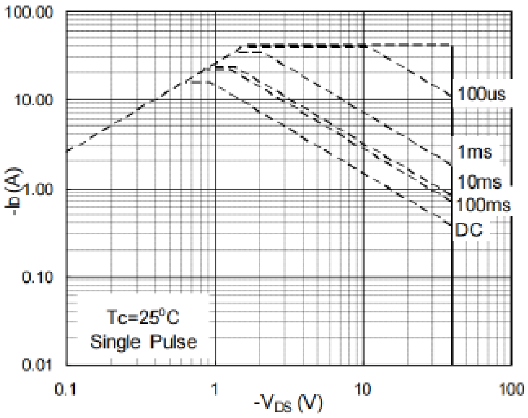


Fig.8 Safe Operating Area

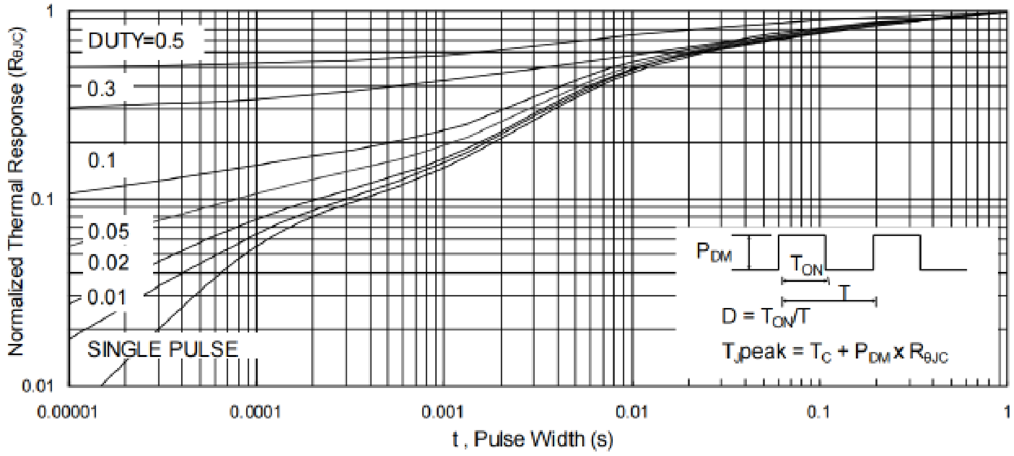
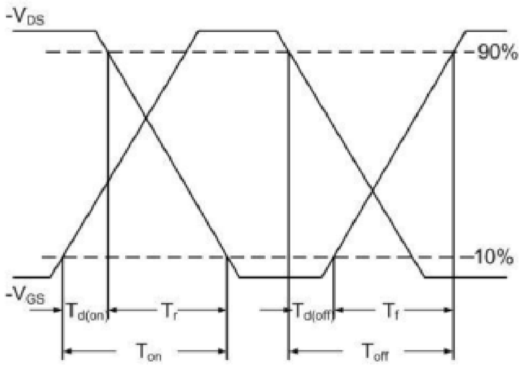
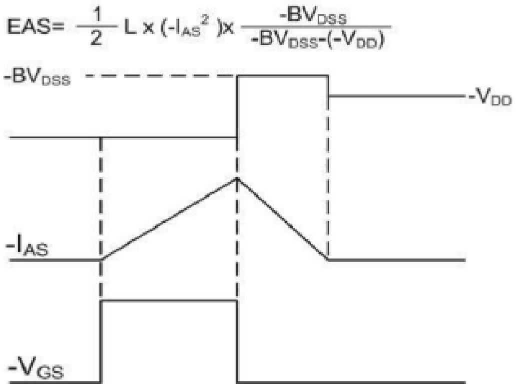


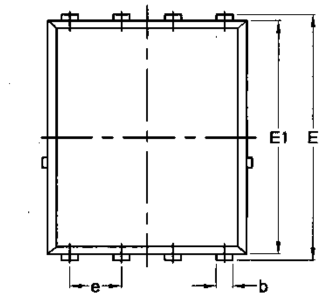
Fig.9 Normalized Maximum Transient Thermal Impedance

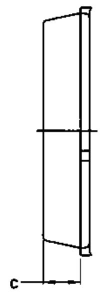


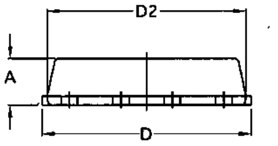


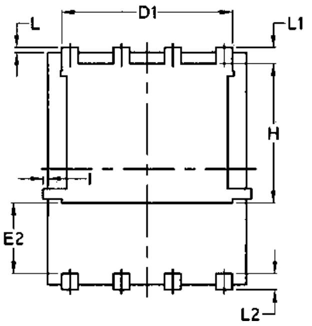
$$EAS=\frac{1}{2}L\times(-I_{AS}{}^{2})\times\frac{-BV_{DSS}}{-BV_{DSS}-(-V_{DD})}$$

Package Mechanical Data-DFN5*6-8L-JQ Single









Symbol	Common			
	mm		Inch	
	Mim	Max	Min	Max
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.0970	0.0324	0.082
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	/	0.0630	/
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
I	/	0.18	/	0.0070