

General Description

The AO4620 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

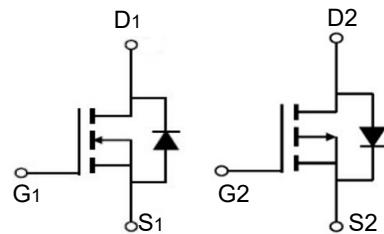
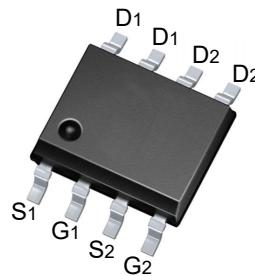


Features

V_{DSS}	30	-30	V
I_D	12	-9.8	A
$R_{DS(ON)}(\text{at } V_{GS}=10\text{V})$	< 12		$\text{m}\Omega$
$R_{DS(ON)}(\text{at } V_{GS}=-10\text{V})$	< 25		$\text{m}\Omega$

Application

- Battery protection
- Load switch
- Uninterruptible power supply



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AO4620	SOP-8	4620	3000

Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Rating		Units
		N Ch	P Ch	
V_{DS}	Drain-Source Voltage	30	-30	V
V_{GS}	Gate-Source Voltage	± 20	± 20	V
$I_D @ T_A=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}^1$	12	-9.8	A
$I_D @ T_A=70^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}^1$	7	-5.2	A
I_{DM}	Pulsed Drain Current ²	36	-26	A
EAS	Single Pulse Avalanche Energy ³	24	72	mJ
I_{AS}	Avalanche Current	22	-38	A
$P_D @ T_A=25^\circ\text{C}$	Total Power Dissipation ⁴	1.5	1.5	W
T_{STG}	Storage Temperature Range	-55 to 150	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	-55 to 150	°C
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	85	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	25	°C/W

N-Channel Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	30	---	---	V
BV _{DSS}	BVDSS Temperature Coefficient	Reference to 25°C, I _D =1mA	---	0.023	---	V/°C
R _{DSON}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =8A	---	---	12	mΩ
		V _{GS} =4.5V, I _D =6A	---	---	18	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.2	---	2.5	V
V _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	-5.08	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =24V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =24V, V _{GS} =0V, T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =8A	---	24	---	S
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	1.8	---	Ω
Q _g	Total Gate Charge (4.5V)	V _{DS} =15V, V _{GS} =4.5V, I _D =8A	---	9.63	---	nC
Q _{gs}	Gate-Source Charge		---	3.88	---	
Q _{gd}	Gate-Drain Charge		---	3.44	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =15V, V _{GS} =10V, R _G =1.5, I _D =8A	---	4.2	---	ns
T _r	Rise Time		---	8.2	---	
T _{d(off)}	Turn-Off Delay Time		---	31	---	
T _f	Fall Time		---	4	---	
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz	---	940	---	pF
C _{oss}	Output Capacitance		---	131	---	
C _{rss}	Reverse Transfer Capacitance		---	109	---	
I _s	Continuous Source Current ^{1,5}	V _G =V _D =0V, Force Current	---	---	9	A
I _{SM}	Pulsed Source Current ^{2,5}		---	---	36	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _s =1A, T _J =25°C	---	---	1	V
t _{rr}	Reverse Recovery Time	I _F =8A, dI/dt=100A/μs, T _J =25°C	---	8	---	nS
Q _{rr}	Reverse Recovery Charge		---	2.9	---	nC

Note :

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
3. The EAS data shows Max. rating. The test condition is V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=21A
4. The power dissipation is limited by 150°C junction temperature
5. The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

P-Channel Typical Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250uA	-30	---	---	V
BV _{DSS}	BVDSS Temperature Coefficient	Reference to 25°C , I _D =-1mA	---	-0.022	---	V/°C
R _{DSON}	Static Drain-Source On-Resistance ²	V _{GS} =-10V , I _D =-6A	---	---	25	mΩ
		V _{GS} =-4.5V , I _D =-4A	---	---	42	
V _{Gsth}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-1.0	---	-2.5	V
V _{Gsth}	V _{Gsth} Temperature Coefficient		---	4.6	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-24V , V _{GS} =0V , T _J =25°C	---	---	-1	uA
		V _{DS} =-24V , V _{GS} =0V , T _J =55°C	---	---	-5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V	---	---	±100	nA
g _{fS}	Forward Transconductance	V _{DS} =-5V , I _D =-6A	---	17	---	S
R _g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz	---	13	---	
Q _g	Total Gate Charge (-4.5V)	V _{DS} =-15V , V _{GS} =-4.5V , I _D =-6A	---	12.6	---	nC
Q _{gs}	Gate-Source Charge		---	4.8	---	
Q _{gd}	Gate-Drain Charge		---	4.8	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =-15V, V _{GS} =-10V, R _G =3.3 , I _D =-6A	---	4.6	---	ns
T _r	Rise Time		---	14.8	---	
T _{d(off)}	Turn-Off Delay Time		---	41	---	
T _f	Fall Time		---	19.6	---	
C _{iss}	Input Capacitance	V _{DS} =-15V , V _{GS} =0V , f=1MHz	---	1345	---	pF
C _{oss}	Output Capacitance		---	194	---	
C _{rss}	Reverse Transfer Capacitance		---	158	---	
I _s	Continuous Source Current ^{1,5}	V _G =V _D =0V , Force Current	---	---	-6.5	A
I _{SM}	Pulsed Source Current ^{2,5}		---	---	-26	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _s =-1A , T _J =25°C	---	---	-1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZcopper.
- 2.The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%
- 3.The EAS data sh.The power dissipation is limited by ows Max. rating
4. The test condition is V_{150C} junction temperature_{DD}=-25 V,V_{GS}=-10V,L=0.1mH,I_{AS}=-30A
- 5 .The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

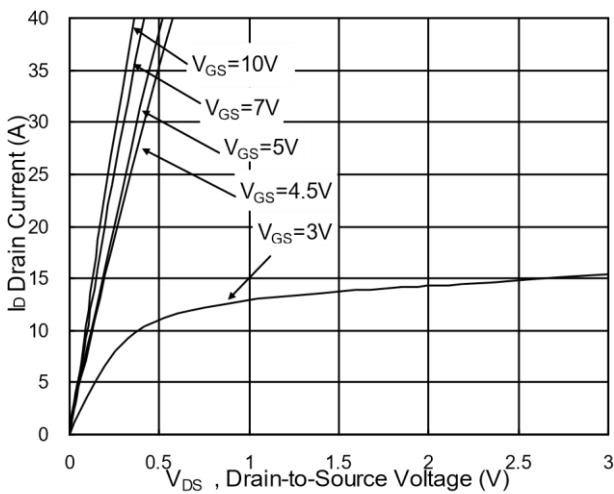


Fig.1 Typical Output Characteristics

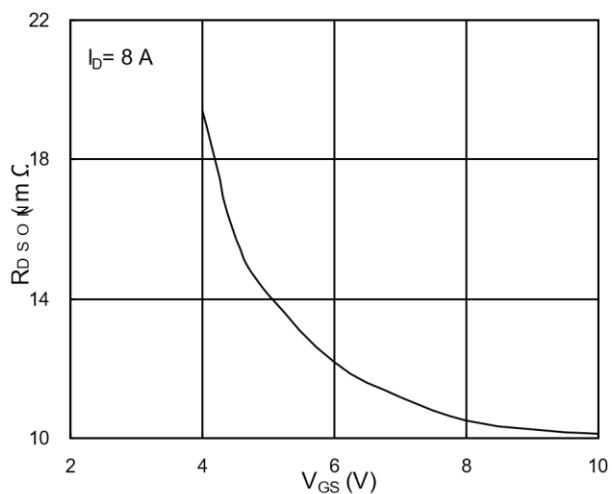


Fig.2 On-Resistance vs. G-S Voltage

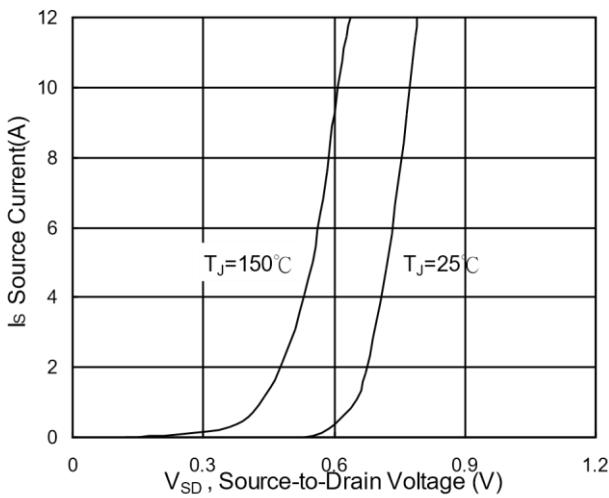


Fig.3 Forward Characteristics of Reverse

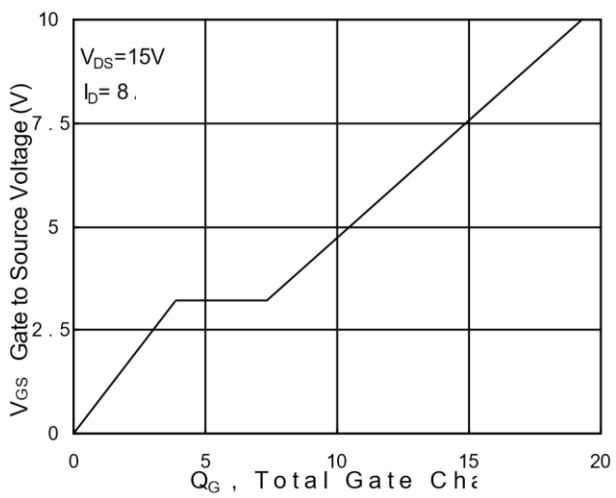
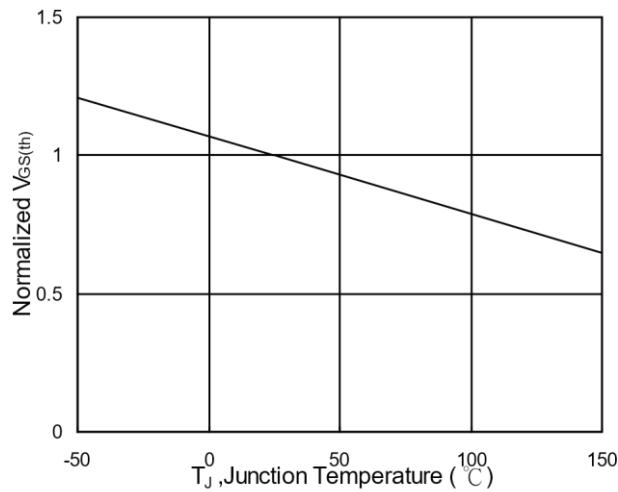
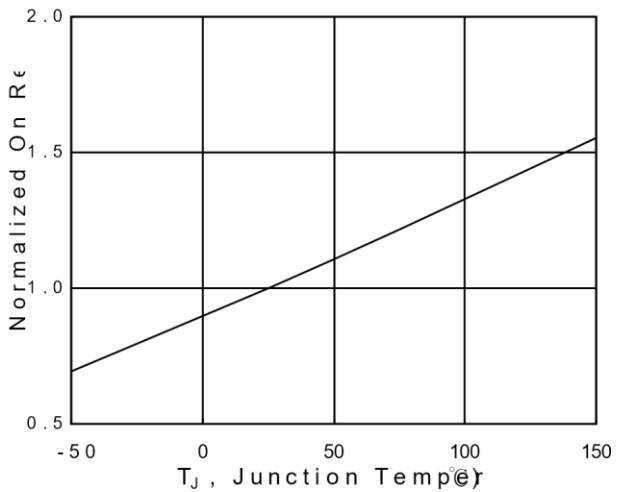


Fig.4 Gate-Charge Characteristics

Fig.5 Normalized $V_{GS(th)}$ vs. T_J Fig.6 Normalized R_{DSON} vs. T_J

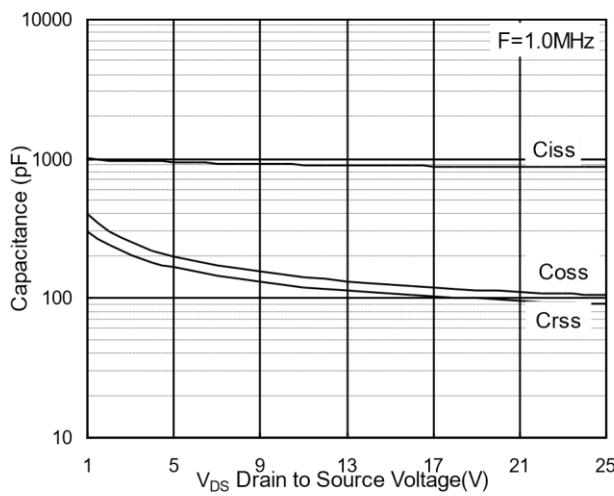


Fig.7 Capacitance

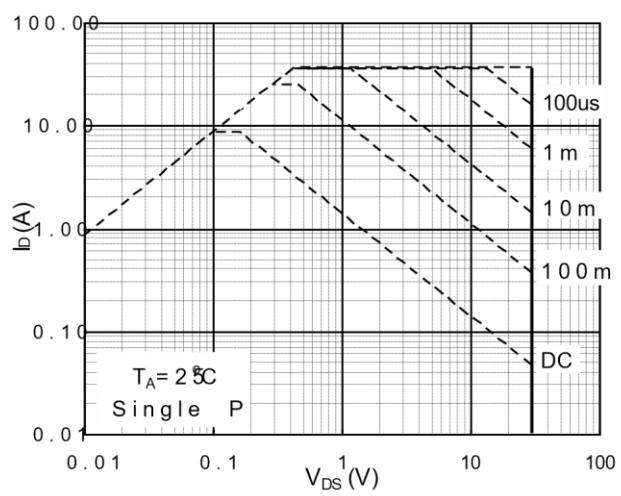


Fig.8 Safe Operating Area

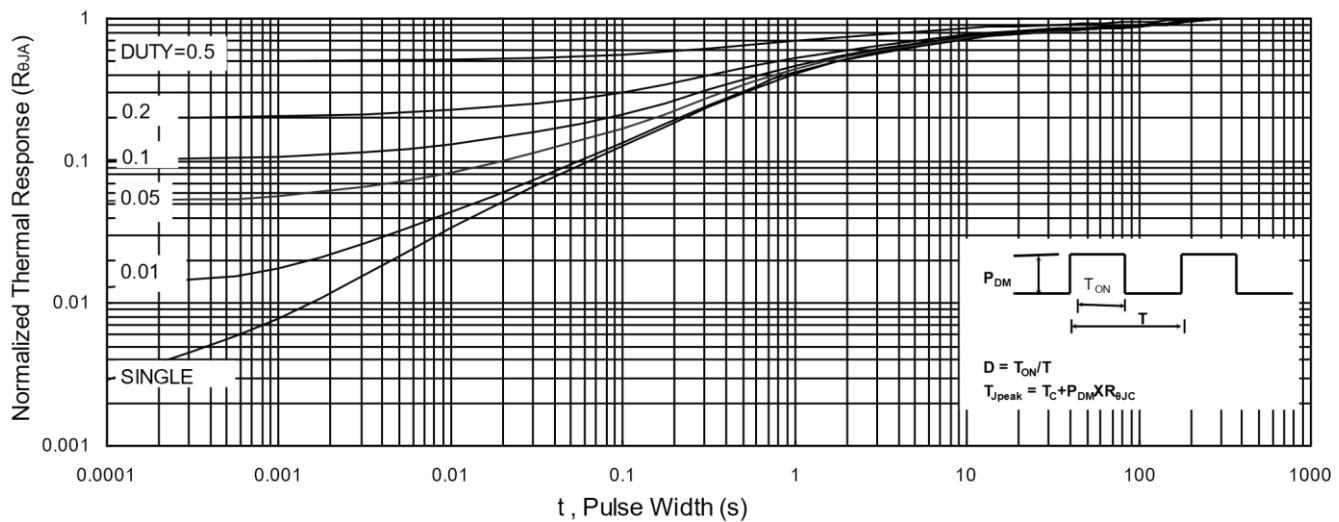


Fig.9 Normalized Maximum Transient Thermal Impedance

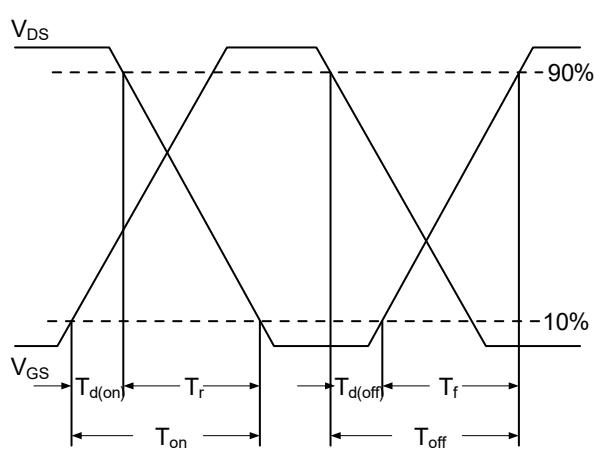


Fig.10 Switching Time Waveform

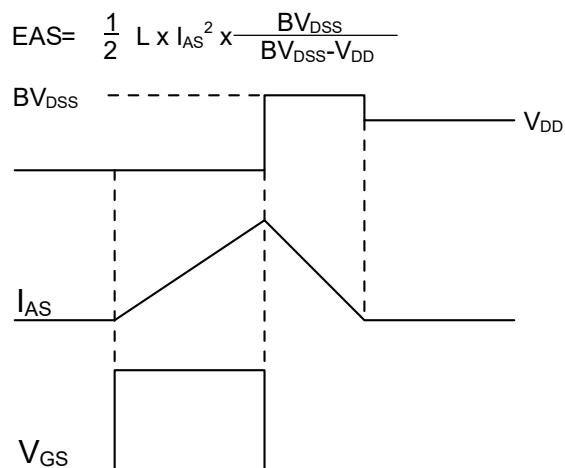


Fig.11 Unclamped Inductive Switching Waveform

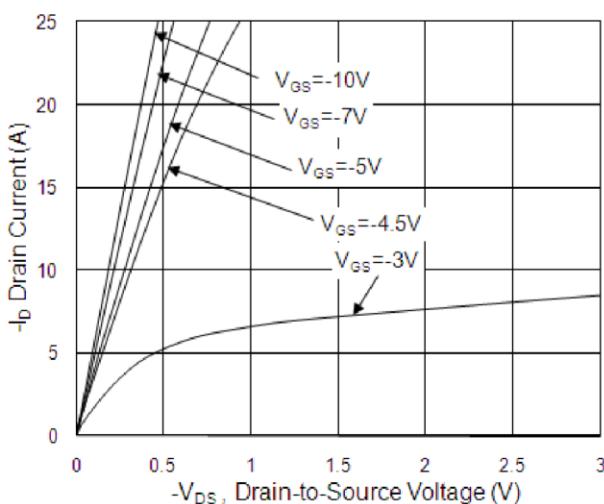


Fig.1 Typical Output Characteristics

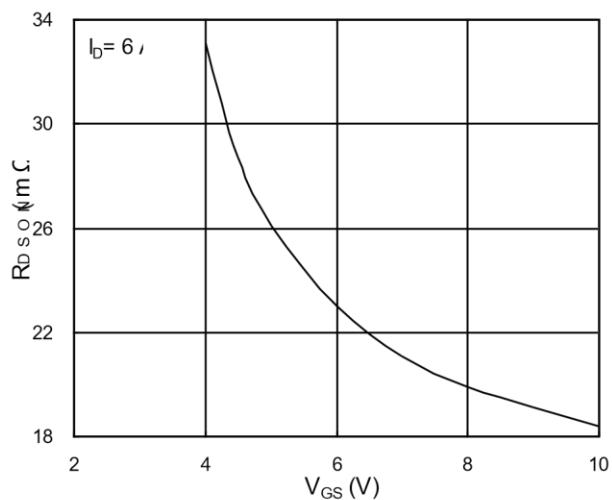


Fig.2 On-Resistance v.s Gate-Source

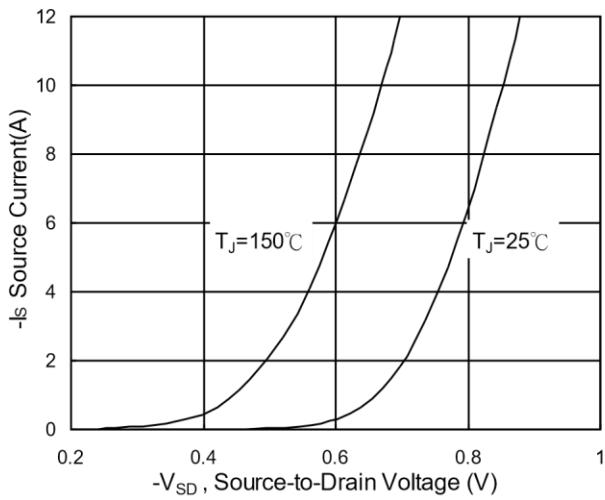


Fig.3 Forward Characteristics of Reverse

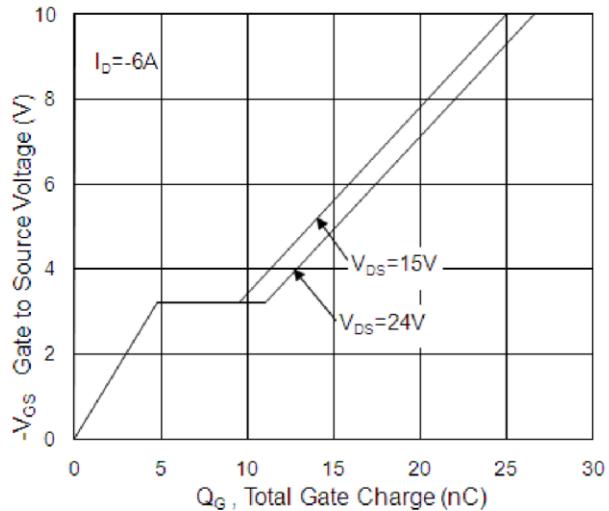
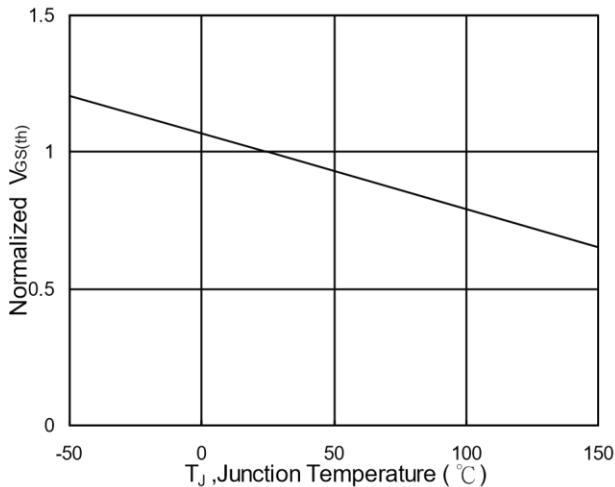
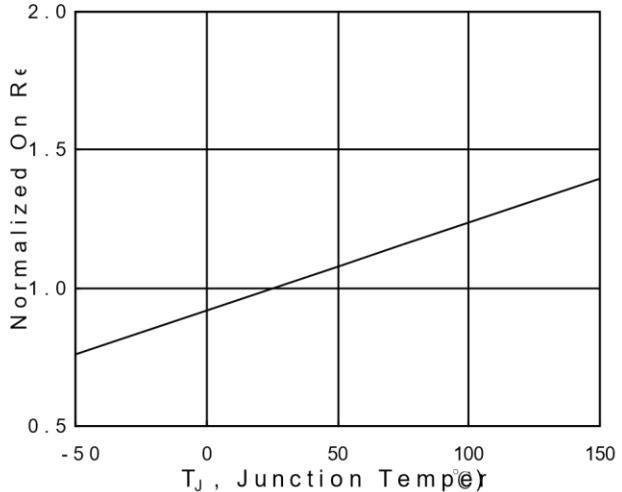


Fig.4 Gate-Charge Characteristics

Fig.5 Normalized $V_{GS(th)}$ v.s T_J Fig.6 Normalized $R_{DS(on)}$ v.s T_J

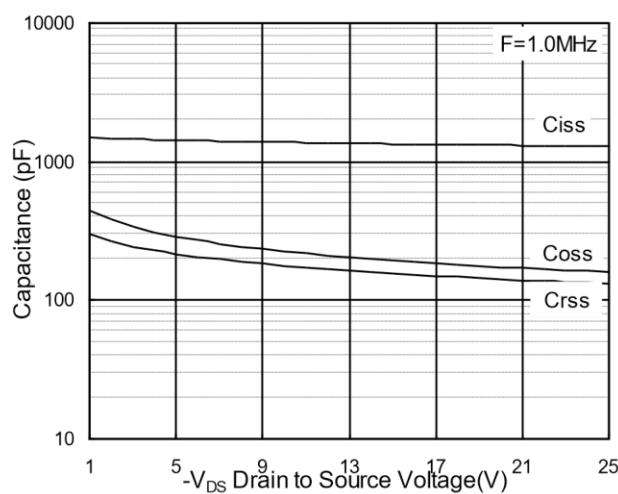


Fig.7 Capacitance

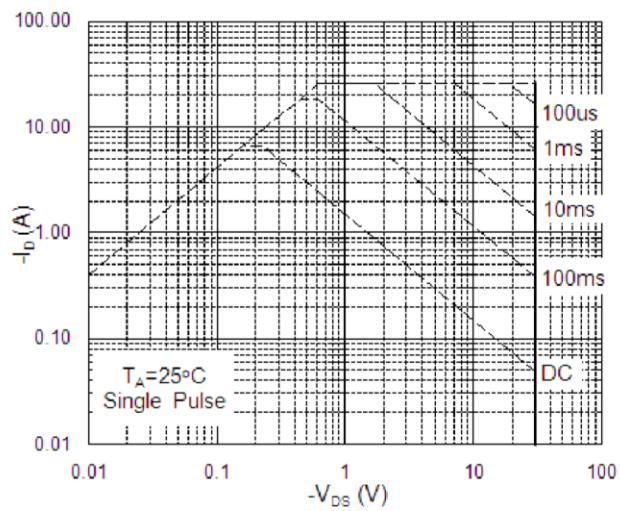


Fig.8 Safe Operating Area

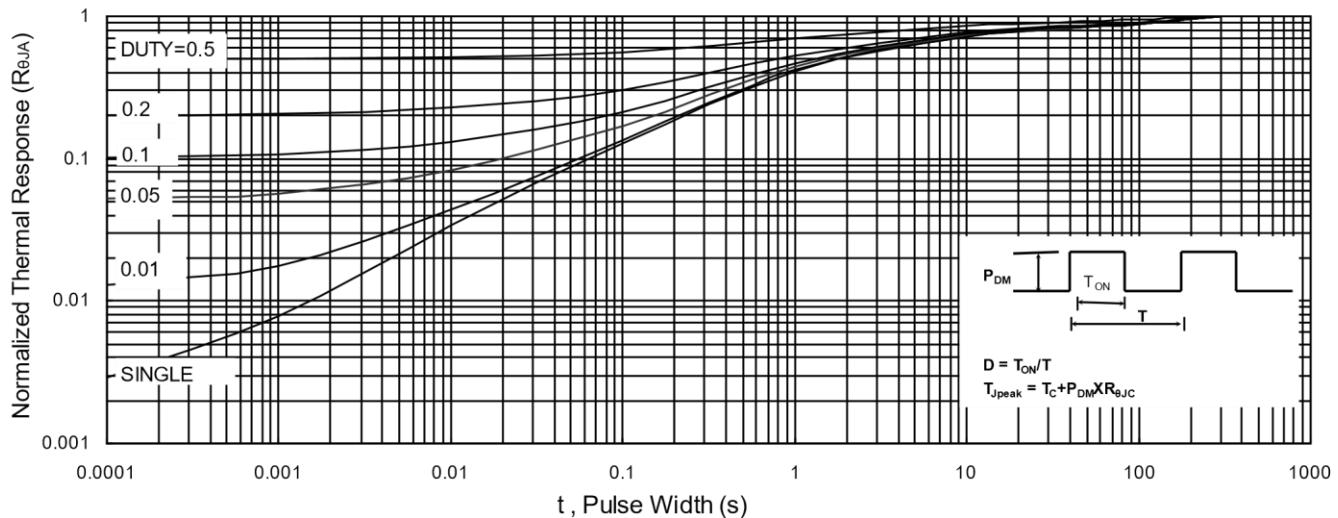


Fig.9 Normalized Maximum Transient Thermal Impedance

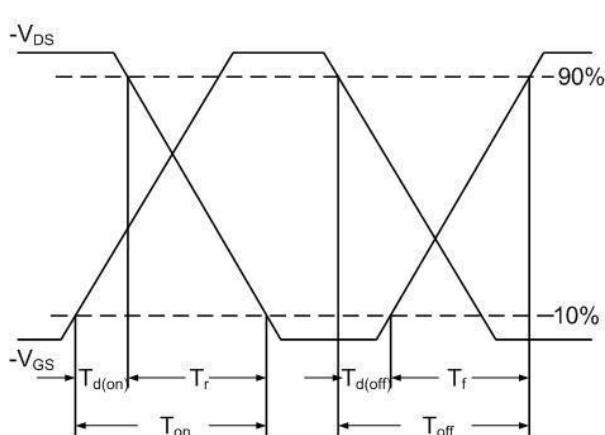


Fig.10 Switching Time Waveform

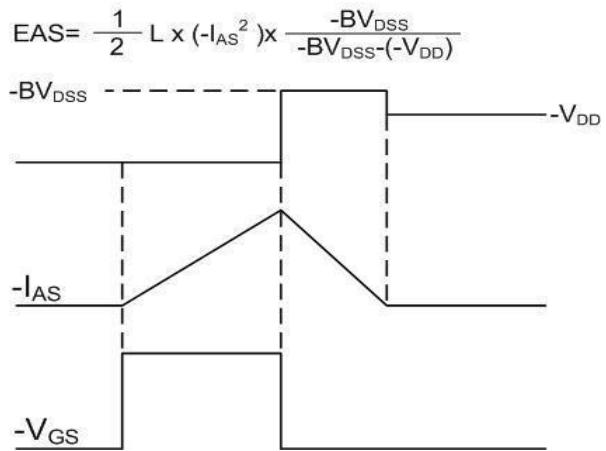
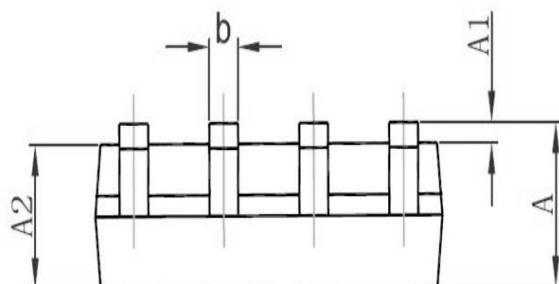
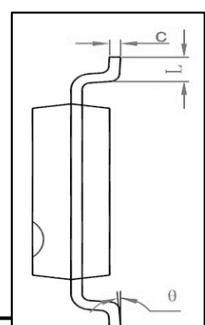
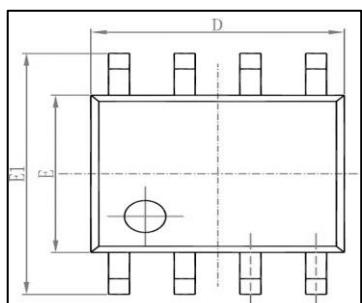


Fig.11 Unclamped Inductive Switching Waveform

Package Mechanical Data-SOP-8



Symbol	Dimensions in Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
theta	0°	8°	0°	8°

